

NAAC Evaluative Report

2015

DAIICT

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Dhirubhai Ambani Institute of Information and Communication Technology



Dhirubhai Ambani Institute of Information and Communication Technology
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Evaluative Report

1. Name of the Programmes

The institute offers the following programmes:

Undergraduate Programme:

- Bachelor of Technology in Information and Communication Technology –B.Tech (ICT) [4-year (8 semesters) degree programme]
- Bachelor of Technology in Information and Communication Technology - B.Tech (Honours in ICT) with minor in Computational Science [4-year (8 semesters) degree programme]

Postgraduate Programme:

- Master of Technology in Information and Communication Technology - M.Tech (ICT) [2-year (4 semesters) degree programme]
- Master of Science in Information Technology - M.Sc (IT) [2-year (4 semesters) degree programme]
- Master of Science in Information and Communication Technology in Agriculture and Rural Development –M.Sc (ICT in ARD) [2-year (4 semesters) degree programme]
- Master of Design in Communication Design - M.Des (CD) [2-year (4 semesters) degree programme]
- Doctor of Philosophy - PhD

2. Year of establishment

DA-IICT was established in 2001. Subsequently, it became a university under the State Act of Gujarat in 2003. It was granted recognition under section 2(f) of UGC and also the membership of Association of Indian Universities (AIU).



29 October, 2001: DA-IICT registered as a Society and Trust under the Societies Registration Act, 1860 and the Bombay Public Trust Act, 1950, respectively (Society Registration No. GUJ/861/GANDHINAGAR) 06 March 2003: Government of Gujarat confers University status on DA-IICT through an Act of Gujarat Legislature (Act No. 6 of 2003)

30 November 2004: DA-IICT gets recognition u/s 2(f) of the UGC Act, 1956 through a Notification of the UGC.

3. Is the Programme part of a School/Faculty of the university?

Yes, DAIICT is unitary University,

4. Names of programmes offered (UG, PG, M.Phil., PhD., Integrated Masters; Integrated PhD., D.Sc., D.Litt., etc.)

The institute offers the following programmes at UG and PG level.

Undergraduate Programme:

- Bachelor of Technology in Information and Communication Technology - B.Tech (ICT)
- Bachelor of Technology in Information and Communication Technology - B.Tech (Honours in ICT) with minor in Computational Science

Postgraduate Programme:

- Master of Technology in Information and Communication Technology - M.Tech (ICT)
- Master of Science in Information Technology - M.Sc (IT)
- Master of Science in Information and Communication Technology in Agriculture and Rural Development –M.Sc (ICT in ARD)
- Master of Design in Communication Design - M.Des (CD)
- Doctor of Philosophy – PhD



5. Interdisciplinary programmes and departments involved

All our programmes are interdisciplinary nature, as ICT itself captures interdisciplinary curricula of Information Technology, Communications Technology and Electronics Engineering. These three domains constitute the hybridity of ICT as an innovative knowledge system at the undergraduate and postgraduate engineering. The curriculum of UG and PG programmes involves innovative strands from Humanities and Social Science courses, namely, Cultural Studies, Art and Science Fiction, Management, Finance, Economics and Environment, Animation, Film, Design, Graphic Design and Multimedia systems. Students of every programme undertake semester long project under the supervision of faculty based on students' choice. This enables students and faculty to work in a true interdisciplinary environment. The curriculum of every programme provides adequate rooms for electives offerings, where faculty members from different specializations can offer courses on his/her field of interests.

6. Courses in collaboration with other universities, industries, foreign institutions, etc.

None. DAIICT is unitary University.

7. Details of programmes discontinued, if any, with reasons

None.

8. Examination System: Annual / Semester / Trimester / Choice Based Credit system

Semester which follows Choice Based Credit System

All our programmes follow semester based examination system which adopts Choice Based Credit System (CBCS). Students are given adequate flexibility in selecting courses they like to consider for their electives and have them count towards their graduation credit requirements. In every course of the programmes, the instructor announces to students about the grading policy and the distribution of weightage in



different components in the courses that the instructor is going to follow to assess the students' performance in the course. The performance of the students is assessed on a continuous evaluation mechanism by assessing their performance in in-semester examinations, end-semester examinations, assignments, quizzes, student presentations and projects. A minimum of 80% attendance is required for students to appear in the end semester examinations. After evaluation, the answer books are shown to the students so that they can see where they have made mistakes and discuss with the instructor accordingly. The semester result is announced within a week after the end semester examination. In every semester, students accumulate their semester performance index (SPI) on a 10-point scale, and then the SPI is accumulated in their cumulative performance index (CPI) on a 10-point scale that will show their academic performance during their study as well as at the end of their graduation. The institute maintains a fully online E-Campus system to manage students' records such as semester-wise course list, student registration, approval processes, result announcement, grading, and reflection of students' SPI and CPI in their grade sheets. The E-Campus system is owned by the Registrar's office of the institute and the entire processes of evaluation and grading system of all our academic programmes are fully transparent to students and faculty.

9. Participation of the department in the courses offered by other departments

All our programmes are interdisciplinary in nature, running under Information and Communication Technology as a discipline. As a result, participation of a large number of elective courses of one programme to other is a need by the design of the programme curriculum. Most of the electives in one programme are open other programmes. The electives consist of the set of technical electives, science electives and open electives. Faculty members actively participate in offering internships and projects to UG and PG students within and outside the curriculum requirement. This is perhaps the unique characteristic of all our programmes that effectively converges students and faculty participation to one discipline, that is, Information and Communication Technology.



10. Number of teaching posts sanctioned, filled and actual (Professors/ Associate Professors/Asst. Professors/others)

All teaching staff are counted as faculty. There is no department division among the faculty. The overall distributions of faculty as per the sanction posts are furnished in the table below. It is to be noted that PhD and M.Tech students are engaged as teaching assistants (TA) in the courses where either laboratory or tutorial or both is included in a course. The main mandate of such TAs is to conduct lab and tutorial session as per the guide line given by the course instructor. Hence in the table teaching assistants are also counted as teaching staff.

Category		Sanctioned	Filled	Actual (including CAS MPS)
Professor		13	14	14
Associate Professor		14	13	13
Assistant Professor		23	23	23
Teaching Assistants	PhD			24
	M.Tech			106
TOTAL				180

11. Faculty profile with name, qualification, designation, area of specialization, experience and research under guidance

Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
1	Nagaraj Ramrao	PhD (Electrical & Electronics Engineering)	Director	Fault tolerant control, Non linear control systems, neural networks and reconfigurable control systems.	26	10	12
2	Anish Mathuria	PhD (Computer Science), University of Wollongong, Australia	Professor	Computer Security	24	1	6
3	Binita Desai	PGD (Animation and Communication Design), National Institute of Design, Ahmedabad, India	Professor	Animation, Communication Design and Multimedia	27	-	-
4	B N Hiremath	PhD (Agricultural Economics), University of Kentucky, USA	Professor	Sustainable rural livelihoods, rural development, natural resources and environmental economics, e-governance	33	2	-
5	Deepak Ghodgaonkar	PhD (Electrical Engineering), University of Utah, USA	Professor	RF & Microwave Engineering, Smart Antennas, Communication and Radar Systems	29	8	6
6	Manjunath Joshi	PhD (Electrical Engineering), IIT Bombay, India	Professor	Computer Vision, Image processing, Super-Resolution, Restoration, Signal Processing, Digital Communication	29	9	17
7	Naresh Jotwani	PhD (Computer Science), Rice University, USA	Professor	Solar energy, engineering design, economics.	37	1	-
8	Ranendu Ghosh	PhD (Soil science & Agricultural Chemistry) Indian Agricultural Research Institute, New Delhi, India	Professor	Satellite Remote Sensing & GIS, Satellite Comm. Applications for Rural Development sustainable agriculture system.	30	2	-



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
9	Sanjeev Gupta	PhD (Communication Engineering), Queen's University, Belfast, U.K.	Professor	Smart Antennas, Communication and Radar Systems, RF/Microwave Applications	27	7	11
10	Sanjay Srivastava	PhD (Physics), University of California, Los Angeles, USA	Professor	Computer Networks, Protocol Design and Modelling	22	4	13
11	Suman Kumar Mitra	PhD (Computer Science), ISI, Calcutta, India	Professor	Image Processing, Pattern Recognition, Fractal, Bayesian Network and Digital Image Watermarking	17	5	10
12	Vishvajit Pandya	PhD (Anthropology), University of Chicago, USA	Professor	Material Culture, Design and Communication Culture, Visual Anthropology, Rituals and History with specific reference to Colonialism	43	1	-
13	V P Sinha	PhD (Electrical Engineering), University of London, England	Distinguished Professor	Digital Signal and Image Processing, Theory of Communication, Discrete Mathematics and Logic, Modern Fourier Theory	47	-	-
14	Aditi Nath Sarkar	M.A. (English), University of Caluatta, India	Associate Professor	Literature, Religious, Cultural History; South Asian	44	1	-
15	Alka Parikh	PhD (Agriculture and Allied Economics), Cornell University, USA	Associate Professor	Rural development and the related issues like Agriculture, Poverty, Unemployment and Wages, Rural Finance, Environment and Development, Disaster Management	22	1	-
16	Amit Bhatt	PhD (Electrical Engineering), North Carolina State	Associate Professor	Multi core Computer architecture and	18	-	16



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
		University, USA		parallel programming. Low Power Methodology in Digital Design			
17	Asim Banerjee	PhD (Electrical Engineering), IIT Bombay, India	Associate Professor	Pattern Recognition, Medical Imaging, Image Processing, Digital Signal Processing, Speech Coding, Software Engineering	26	2	8
18	Biswajit Mishra	PhD (Electrical & Electronics Engineering), University of South Hampton, UK	Associate Professor	Ultra Low Power and Sub-threshold Circuit Methodologies, Very Low Voltage Circuits for Wireless Sensor Networks	7	3	12
19	Hemant A Patil	PhD (Speech processing), IIT Kharagpur, India	Associate Professor	Speaker recognition and wavelet signal processing	12	6	35
20	Madhumita Mazumdar	PhD (History), University of Calcutta, India	Associate Professor	Social history of Science, Technology and Medicine in India, cultures of communication and the media	16	1	-
21	Manik Lal Das	PhD (Information Technology), IIT Bombay, India	Associate Professor	Information Security, Cryptography, System Design and Analysis	16	4	8
22	Manish kumar Gupta	PhD (Mathematics), IIT Kanpur, India	Associate Professor	Bio-molecular computing, Coding & Information theory, Cryptology, Quantum computing, Computational, Structural & Systems Biology and Bioinformatics	14	3	-
23	Pokhar Mal Jat	PhD (Computer Science), ML Sukhadia University, Udaipur, India	Associate Professor	Databases, Data Mining, Web of Data, Software Design	27	-	5
24	Radha Parikh	PhD (Special Education),	Associate	Communication,	11	-	-



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
		University of Missouri-Columbia, USA		Value Education, Constructivist approach to Teaching & Learning, e-learning.			
25	Srikrishnan Divakaran	PhD (Computer Science), Rutgers University, USA	Associate Professor	Design and Analysis of Algorithms for problems in Bioinformatics, Machine scheduling & Distributed systems	18	1	5
26	Sunitha Murugan	PhD (Mathematics), IIT Madras, India	Associate Professor	Algorithms, Discrete Mathematics, Graph Theory, Parallel & Distributed Computing, Theoretical Computer Science, Interconnection Networks	8	1	4
27	Rahul Dubey	PhD (Electrical Engineering), IIT Roorkee, India	Associate Professor	Design and Prototyping of Digital systems, Factory Automation	17	1	8
28	Aditya Tatu	PhD (Image Analysis), University of Copenhagen, Denmark	Assistant Professor	Applications of Differential geometry (shapes, curve evolutions etc.) Image features, Continuous optimization.	5	3	7
29	Anil Kumar Roy	PhD (Physics), IIT Delhi, India	Assistant Professor	Fibre Optics and Optical Communication, Quantum Optics, Nanotechnology, Semiconductor devices, ICT Applications in Rural Development	25	1	1
30	Amishal Modi	M.A. (English), Ohio State University, USA	Assistant Professor	English language Teaching Victorian literature indian poetry in English	11	-	-
31	Bharani Kollipara	PhD (English Literature), English & Foreign	Assistant Professor	Literature and Philosophy,	10	-	-



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
		Languages University, Hyderabad, India		Ancient Greek Philosophy, Political Theory, Aesthetics			
32	Bhaskar Chaudhury	PhD (Physics), Institute for Plasma Research, Gandhinagar, India	Assistant Professor	Computational Plasma Physics, Computational Electromagnetics, High Performance Computing, Scientific Data Management	13	-	1
33	Dharamsingh Karmyal	M.P.Ed. (Physical Education)	Assistant Professor	Sports	37	-	-
34	Gagan Garg	PhD (Computer Science), IISc Bangalore, India	Assistant Professor	Information theory, cryptography, number theory	4	-	-
35	Ganesh Bagler	PhD (Computational Biology), CSIR-CCMB Hyderabad and Jawaharlal Nehru University, New Delhi, India	Assistant Professor	Computational and Systems Biology, Complex Systems, Complex Networks	7	6	2
36	Gautam Dutta	PhD (Physics), Gujarat University, India	Assistant Professor	Quantum Computers, Signal Processing, Image Processing, Particle Physics	17	-	-
37	Jaideep Mulherkar	PhD (Mathematics), University of California, Davis, USA	Assistant Professor	Mathematical Physics, Quantum Computation and Information	15	-	-
38	Laxminarayana Pillutla	PhD (Electrical Engineering), University of British Columbia, Canada	Assistant Professor	Statistical signal processing, information theory, game theory, non-linear optimization, wireless sensor networks, cognitive radio, cross layer design of wireless networks.	13	1	6
39	Manoj Kumar Raut	PhD (Mathematics), IIT Madras, India	Assistant Professor	Logic in Computer Science	10	0	1
40	Minal Bhise	PhD (Computer Science), BITS, Pilani, India	Assistant Professor	Semantic Web, Distributed	22	2	10



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
				Databases, Software System Analysis and Design			
41	Mukesh Tiwari	PhD (Optical Science & Engineering), University of New Mexico, USA	Assistant Professor	Statistical Physics, Non Linear Dynamics, Quantum Transport, Surface Science	6	-	-
42	Nabin Kumar Sahu	PhD (Mathematics), IIT Kharagpur, India	Assistant Professor	Applied Functional Analysis, Operator Theory, Variational Inequality, Variational Inclusion Problems	1	-	-
43	Prasenjit Majumder	PhD (Computer Science), Jadavpur University, Kolkata, India	Assistant Professor	Information Retrieval, Natural Language processing, Digital Libraries	15	4	10
44	Puneet Bhateja	PhD (Computer Science), Chennai Mathematical Institute, India	Assistant Professor	Formal Methods used for Testing and Verification	5	-	-
45	Purushothaman A	PhD (Information & Communication Technology), DA-IICT, Gandhinagar, India	Assistant Professor	Analog & Mixed circuit Design, Cmos digital Integrated Circuits, Low power VLSI.	10	-	24
46	Rahul Muthu	PhD (Computer Science), Homi Bhabha National Institute, Mumbai, India	Assistant Professor	Graph theory and algorithms	13	1	1
47	Rutu Parekh	PhD (Electrical Engineering), Shrebrooke University, Quebec, Canada	Assistant Professor	Nanoelectronics, nano device-CMOS hybridization, design and simulation, circuit design, modeling and simulation of next-generation memory (PCM), nanofabrication.	14	1	6
48	Saurabh Tiwari	PhD (Computer Science) (submitted), IIITDM Jabalpur, India	Assistant Professor	Requirement Engineering, Empirical Software Engineering, Evidence-based Software Engineering	-	-	-
49	Shweta Garg	PhD (English), IIT Roorkee, India	Assistant Professor	Food & Cultural Studies,	5	-	-



Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience	No. of Ph.D./M.Tech students guided for the last 4 years	
						PhD	M. Tech
				Performance Studies, Creative Writing, Literature of the Indian Diaspora			
50	Sourish Dasgupta	PhD (Computer Science), University of Missouri-Kansas City, USA	Assistant Professor	Distributed Multi-Agent System, Service Oriented Architecture, Semantic Web	8	1	7

12. List of senior Visiting Fellows, adjunct faculty, emeritus professors

Sr.No.	Name	Qualification	Designation	Area of Specialization	Experience
1	Dipankar Nagchoudhuri	PhD (Electrical Engineering, Michigan State University USA)	Adjunct Faculty	VLSI Design, CMOS Circuits And Technology, Biomedical Signal Processing Chip Design.	35
2	Khushru F Doctor	CSM, PMP, CISA, Six Sigma GB	Adjunct Faculty	Software Engineering and Management	25
3	Nikhil Raval	MBA (Strategic Management) California State University, USA	Adjunct Faculty	Strategy and Finance	18
4	Narendra Patel	Dip. in Fine Arts, Kala Niketan, (Now Govt. Inst. Of Fine Arts) Jabalpur Dip. In Visual Communication (Animation Film) NID Ahmedabad	Adjunct Faculty	Film & Animation film, HFX, Communication Design, eLearning, Photography, Web Design, Multimedia , Graphics user Interface, Software Development, Printing Technology.	18
5	Kuntala Dasgupta	B.Sc Calcutta University, West Bengal Gita Bharati (Specialization: Rabindra Sangeet) Gitabitan, Kolkata, West Bengal	Adjunct Faculty	Rabindra Sangeet (beginner, intermediate, advanced) North Classical(beginner ,intermediate) Nazrul Geeti(beginner)	20
6	Bhavesh Patel	BE in IT, Master of Design (DA-IICT), Gandhinagar,	Adjunct Faculty	Video and Photography	5



		Diploma in Professional Photography Light & Life Academy Ooty			
7	Kaushik Brahmhatt	B.com, PG in Journalism, Public Relation & Advertising, PG Commercial Photography, PG Performing Art-Puppetry	Adjunct Faculty	Video and Photography	15
8	Dixsha Sisodia	Ph.D Faculty of Management Studies, Mohanlal Sukhadia University, Udaipur Master of Business Administration Department of Management Studies, Indian Institute of Technology, Roorkee	Adjunct Faculty	Operations Management, Project Management, Finance for Strategic Decisions and IT Specialization Subjects	7
9	Ashish Phophalia	MTech in ICT from DA-IICT Gandhinagar	Adjunct Faculty	Pattern Recognition and Medical Image Processing	3
10	Naveen Kumar	MTech, GGIPS, Delhi	Adjunct Faculty	Computer Network Security	8
11	Indrani Choudhury Singh	Ph.D. (Environmental Science), Space Applications Centre, ISRO, Ahmadabad, in association with Kalyani University,	Adjunct Faculty	Environmental studies, GIS and Remote sensing	17
12	Shalini Dey	PG in Information & Digital Design NID Ahmedabad	Adjunct Faculty	Information Design, User Experience Design Infographics,	6

PhD Teaching Assistants

Sr No	Name	Guide	Area of Work	Year of Registration
1	Anshu Chitora	Hemant Patil	Discrete Mathematics	July'11
2	Maulik Chandulal Madhavi	.Hemant Patil	Digitan Signal Processing	July'11
3	V. Ram Naresh Kumar	B N Hiremath	ICTARD COURSES	July'11
4	Nilesh kumar Vaishnav	Aditya Tatu	Signals and Systems	July'11
5	Sarita Agrawal	M L Das	Introduction to ICT and Computational Science, Computer Networks	July'11
6	Shrishail Sharad Gajbhar	M V Joshi	Computer Basics	Dec-11
7	Archana Nigam	Sanjay Srivastava	Introduction to Programming Lab	July-14
8	Sumukh Bansal	Aditya Tatu	High Performance Computing	July-14
9	Patel Nikitaben Ratilal	Dean-Academic Programs	VLSI Design	July-14
10	Hardik Gajera	M L Das	Advanced Mathematical Methods	Juyl-14
11	Desai Nidhi Nitinbhai	M L Das	Computer Organization	July'15
12	Rahul Vashisth	Deepak Ghodgaonkar	Electromagnetic Theory	July'15
13	Sujata	Minal Bhise	Database Management Systems	July'15
14	Rishikant R Rrajdeepak	Dean-Academic Programs	Algebraic Structures	July'15
15	Madhulika Agrawal	Prasenjit Majumder	Database Management Systems	July'15
16	Patel Purviben Jayprakash	Rutu Parekh	Embedded Hardware Design	July-12
17	Shaikh Mohammedsayeemuddin kalimuddin	Dean-Academic Programs	Introduction to Programming	Jul-12
18	Padiya Trupti Jayantilal	Minal Bhise	Object Oriented Programming Using JAVA	July-12
19	Krishna Gopal	Manish Kumar Gupta	Calculus and Complex Variables	Dec-12
20	Nupur Jain	Biswajit Mishra	Basic Electronic Circuits	Dec-12
21	Pande Sneha Pramod	Sourish Dasgupta/ Amitsengupta	Communication Skills (P/F)	Dec-12
22	Dixita Limbachiya	Manish Kumar Gupta	Calculus and Complex Variables	July-13
23	Jadeja Mahipal	Rahul Muthu Srikrishnan Divakaran	Algebraic Structures	Dec-12
24	Kamal Manharlal Captain	Manish Kumar Gupta	Calculus and Complex Variables	July-13

M.Tech Teaching Assistants

Sr.No.	Name	Specialization
1	Shweta Mudliar	Computer Networks
2	Jaykumar Kamleshbhai Patel	VLSI And Embedded Systems
3	Madhu Prathmesh Rajeshbhai	Communication Systems
4	Rao Sushant Vijay	Communication Systems
5	Parikh Sagar Samirbhai	VLSI And Embedded Systems
6	Shruti Gupta	VLSI And Embedded Systems
7	Parmar Palas Rajesh	VLSI And Embedded Systems
8	Rahul Nale	VLSI And Embedded Systems
9	Ankur Pokhara	VLSI And Embedded Systems
10	Hardik Bharat Meisheri	Computer Networks
11	Shah Kushan Rajnikant	Computer Networks
12	Thakkar Nileshkumar Hareshbhai	VLSI And Embedded Systems
13	Jyotsana Khatri	Machine Intelligence
14	Ankit Gupta	VLSI And Embedded Systems
15	Kale Vishwamber Niwarttirao	VLSI And Embedded Systems
16	Thakkar Devang Jayantilal	VLSI And Embedded Systems
17	Shweta Jain	VLSI And Embedded Systems
18	Dave Ainish Jyotindra	Computer Networks
19	Rathod Hari Dilipkumar	VLSI And Embedded Systems
20	Bhungaliya Nikita Kanjibhai	Communication Systems
21	Arjun Londhey	Computer Networks
22	Chandra Shekhar Kumar	Communication Systems
23	Ketan Gupta	Communication Systems
24	Surabhi Sohoney	Communication Systems
25	Manisha Sharma	Communication Systems
26	Bhavsar Himanshu Narayandas	Communication Systems
27	Parikh Ketul Dilipkumar	Machine Intelligence
28	Vyas Hardik Shyam	Communication Systems
29	Manish kumar Mangukiya	Machine Intelligence
30	Hanish Kumar Kathpal	Machine Intelligence
31	Ankush Chander	Machine Intelligence
32	Ankush Chander	Machine Intelligence
33	Suthar Dip Dneshkumar	Communication Systems
34	Arohi Arunkumar Patel	Computer Networks
35	Dave Ishaan Rajendra	Communication Systems
36	Meetkumar Hemakshu Soni	Machine Intelligence
37	Anshiki Saxena	Computer Networks
38	Dhruv Haresh Shah	Machine Intelligence
39	Krunal Panchal Ganapatlal	Computer Networks
40	Baghel Sonal Ashokkumar	Computer Networks
41	Ritu Sharma	Computer Networks
42	Bathiya Bhavika Bhupatbhai	Computer Networks
43	Jitendra Gupta	Machine Intelligence



Sr.No.	Name	Specialization
44	Davda Abhishek Arvindbhai	Machine Intelligence
45	Rupsa Saha	Machine Intelligence
46	Patel Brijeshkumar Mmukeshbhai	Computer Networks
47	Pedhadiya Niravkumar Sureshbhai	VLSI And Embedded Systems
48	Desai Meet Nitin	Algorithmics
49	Patel Vismay Navinkumar	VLSI And Embedded Systems
50	Sharma Ayushi Ramprakash	V LSI And Embedded Systems
51	Shah Akash Pravinchandra	Machine Intelligence
52	Mishra Ajay Surendra	VLSI And Embedded Systems
53	Dholariya Pankajkumar Vrajlal	Communication Systems
54	Seksaria Khushboo Suryakant	Algorithmics
55	Harshit Bhatnagar	VLSI And Embedded Systems
56	Trivedi Shaili Nareshkumar	Machine Intelligence
57	Badgujar Jignesh Santoshbhai	VLSI And Embedded Systems
58	Agrawal Jatin Bharat	VLSI And Embedded Systems
59	Tushin Shrotriya	VLSI And Embedded Systems
60	Harshit Pratik	VLSI And Embedded Systems
61	Jainikkumar Pravinchandra Ranpura	VLSI And Embedded Systems
62	Popat Nirali Dineshbhai	Computer Networks
63	Rajput Pruthvish Rajesh	VLSI And Embedded Systems
64	Shihora Rutvi Nayankumar	Algorithmics
65	Karavadra Raju Duda	Communication Systems
66	Kadiya Bhaumik Ashwinkumar	VLSI And Embedded Systems
67	Mansi Singh	Communication Systems
68	Anurag Chintman Ingle	Communication Systems
69	Neelasha Sen	Machine Intelligence
70	K. Hemantha	Computer Networks
71	Christian Ruzvelt Rameshbhai	Algorithmics
72	Tandel Deep Ishvarbhai	Signal Processing
73	Batavia Darshan Naresh	Signal Processing
74	Mulla Zubain Chandsaheb	Algorithmics
75	Anubha Jain	Algorithmics
76	Maral Vishal Rangnath	Computer Networks
77	Apeksha Jagdishbhai Naik	Signal Processing
78	Dharmeshkumar Maheshchandra Agrawal	Signal Processing
79	Dhaval Patel	Algorithmics
80	Rathod Samkit Dineshbhai	Machine Intelligence
81	Priyanka Sharma	Signal Processing
82	Rishabh Agarwal	Algorithmics
83	Bhanushali Artiben Kamleshkumar	Communication Systems
84	Pooja Tiwari	Computer Networks
85	Sayan Chakraborty	Machine Intelligence
86	Sadhvani Jay Dilipkumar	Machine Intelligence
87	Sharma Sushmit	Computer Networks
88	Saurabh Tyagi	Computer Networks

Sr.No.	Name	Specialization
89	Mayank Dubey	Communication Systems
90	Ankit Paliwal	Machine Intelligence
91	Pradip Tilala	Computer Networks
92	Rahul Goel	Computer Networks
93	Digant Dilipbhai Doshi	Computer Networks
94	Priya Ahuja	Communication Systems
95	Surabhi Jain	Communication Systems
96	Mangukiya Chiragkumar Pravinbhai	Communication Systems
97	Kamlesh Karki	Machine Intelligence
98	Vinay Lata	Communication Systems
99	Lathiya Mayur Narottambhai	Signal Processing
100	Rishabh Tak	Signal Processing
101	Satyam Satyajeet	Computer Networks
102	Kotak Nishith Ashokkumar	Signal Processing
103	Dave Keval Narayanbhai	Machine Intelligence
104	Shruti Rajendrakumar Naik	Computer Networks
105	Thakkar Shaival Yogesh	Machine Intelligence
106	Dhwani D Patel	Computer Networks

13. Percentage of classes taken by temporary faculty – programme-wise information

None.

14. Programme-wise Student Teacher Ratio

OVERALL FACULTY-STUDENT RATIO: 1:10

15. Number of academic support staff (technical) and administrative staff:

sanctioned, filled and actual

	Sanctioned	Filled	Actual
Admin	107	107	107
Technical	17	17	17
TOTAL			124

16. Research thrust areas as recognized by major funding agencies

There are focused research groups in VLSI and embedded systems, networks and security, speech and signal processing, pattern recognition and image processing, information retrieval, RF and microwave engineering, computational biology, algorithmics, etc. A list of some of the research groups is given below.



<http://irlab.daiict.ac.in/>

<http://sites.google.com/site/speechlabdaiict/>

<http://security.daiict.ac.in>

<http://www.guptalab.org/>

<http://ictard.daiict.ac.in/>

<http://magnet.daiict.ac.in/>

<http://vlsi.daiict.ac.in/>

<http://wireless.daiict.ac.in/>

<http://prip.daiict.ac.in/>

The Institute has been receiving substantial grants from many government agencies including DST, DIT, DAE, DRDO, and Department of Space. The Institute also has research funding from TCS for supporting selected PhD scholars.

The Incubation Centre which was started in 2007 has received a grant amounting to 150 lakhs from DeitY for promoting start-ups. The centre has incubated six start-up companies till date.

DAIICT has been selected to be a CUDA teaching center by NVIDIA.

Some of the Research Projects funded by various Government agencies is enumerated the table below:

Year completed	Project title	Sponsoring Authority	Total Cost (in lacs)
2015	Design, Development & Hardware implementation of BPSK,QPSK & other module schemes as part of software defined radio(SDR)/ Cognitive Radio for in Satcom terminals	ISRO/DOS	81.69
2015	Service Oriented architecture for spatial data integration and spatial reasoning	DST	35.04
2015	Evaluation of spatiotemporal dynamics of land surface evapo transpiration and monsoon rainfall coupling over Indian region for climate change studies	DST	18.95
2014	Wireless Telemedicine Using Body Area Sensor Networks and	SERC-DST	12.60



	Heterogeneous Access Networks		
2014	Securing Biometric data using data hiding techniques	BRNS/DAE	13.03
2013	Sensor Network Test-Bed for Tokamak Environment	BRFST	29.06
2013	Security Proofs and Multidisciplinary Evaluation for Dynamic Key Assignment Schemes	DST (Indo-Japan)	29.36
2013	Security and Privacy Infrastructure for internet of Things-Scenarios and Applications	DST (Indo-Spain)	21.44
2013	Distortion and Accuracy Improvement in Sample and Hold Circuits for Analog-Digital Converters	DST	18.04
2013	Robust Ultra-Low-Power Double Gate MOSFET Design of Analog, Digital and SRAM Memory Circuits	IFCPAR	8.92
2013	Finger/ Wrist mounted Universal Remote Control for CP Patient	National Trust	12.65
2013	Earth Model for Wireless Sensor Nodes for Detection of Water on Moon/ Water Sensing Systems based on Tuned Diode Laser for Planetary Mission	PRL	58.42

17. Number of faculty with ongoing projects from a) national b) international funding agencies and c) Total grants received. Give the names of the funding agencies, project title and grants received project-wise.

There are 11 ongoing sponsored projects. A total of 13 faculty members are associated with these projects as PI or co-PI.

Year started	Name of the project	Funding agency	PI/co-PI	Total cost (in Lakhs)
2015	Techno Feasibility Study on Automation of hydroponics and green house cultivation	Gujarat Horticulture Mission	Ranendu Ghosh/ Rahul Dubey	3,72,000/-
2015	Knowledge Compilation in modal and Multimodal Logic	National Board for Higher Mathematics	Manoj Raut/ Rahul Muthu	6,84,200/-
2012	Developing of Infant Cry Analyzer using source and system features	DST	Hemant Patil	5,96,000/-
2011	Value Addition in Grassroots Technologies	National Innovation Foundation	Anil Roy/ Rahul Dubey	9,59,271/-
2014	Center Early Adopter – NSF / TCPD CDER	National Science Foundation	Bhaskar Chaudhury/ Mukesh Tiwari	USD 2500
2014	Ultra wide band Dielectric	SAC-RESPOND	Deepak	11,65,000/-



	Resonators Antenna		Ghodgaonkar/ Sanjeev Gupta	
2014	Speech based Access of Agriculture Commodity Prices and Weather Information in 12 Indian Languages/ Dialects (ASR) Consortium-Phase-II	DeitY	Suman Mitra	44,70,000/-
2013	Techniques for robust face recognition with pose variation	BRNS/DAE	Suman Mitra	21,92,000/-
2011	Development of Text to Speech System in Indian Languages Phase-II	DeitY	Hemant Patil/ Manjunath Joshi	76,90,000/-
2011	Development of Cross Lingual Information (CLIA) System Phase-II	DeitY	Prasenjit Majumder/ Suman Mitra	70,84,000/-
2011	Indian Digital Heritage (IDH-Hampi) Phase-II (Digital Capture of Culture & Heritage)	DST	Manjunath Joshi/ Hemant Patil	37,60,000/-
2009	Expansion of Technology Incubation and Development of Entrepreneurs (TIDE) in the areas of Electronics and ICT	Deity	Anish Mathuria/ Manish Gupta	1,50,00,000 /-

18. Inter-institutional collaborative projects and associated grants received

a) National collaboration

Of the five ongoing projects, three project are in collaboration with IIT-Madras and IIT-Bombay, one in collaboration with IIIT-Hyderabad, and one in collaboration with NIAS-Bangalore.

Year started	Title	Name of collaborative Agency/ Institute	Sponsoring Authority	Total Cost (in Lakh)
2014	Speech based Access of Agricultural Commodity Prices and Weather Information in 12 Indian Languages/ Dialects (ASR Consortium-Phase-II)	IIT-Madras	DeitY	44.7
2012	Development of Text to speech system in Indian Languages Phase-II	IIT-Madras	DeitY	76.9
2011	Development of Prosodically Guided Phonetic Engine for searching speech database in Indian Languages	IIIT-Hyderabad	DeitY	50.6



2011	Development of Cross-Lingual Information Access (CLIA) System Phase-II	IIT-Bombay	DeitY	70.84
2011	Indian Digital Heritage (IDH-Hampi) Phase-II (Digital Capture of Culture & Heritage)	NIAS-Bangalore	DST	37.6

b) International collaboration

Year started	Title	Name of collaborative Agency/ Institute	Sponsoring Authority	Total Cost (in Lakh)
2014	High Performance Computing for Computational Science	Centre for Parallel and Distributed Computing Curriculum Development and Educational Resources (CDER)	NSF/TCPP	USD\$2 500

19. Departmental projects funded by DST-FIST; UGC-SAP/CAS, DPE; DBT, ICSSR, AICTE, etc.; total grants received.

The Incubation Centre which was started in 2007 has received a grant from DeitY for promoting start-up companies. The centre has incubated six start-up companies till date.

Year started	Name of the project	Funding agency	Total cost (in Lakhs)
2009	Expansion of Technology Incubation and Development of Entrepreneurs (TIDE) in the areas of Electronics and ICT	Deity	1,50,00,000/-

20. Research facility / centre with

- state recognition
- national recognition
- international recognition

The following research laboratories have been recognized by national funding agencies such as DeitY.

- Information Retrieval Lab (<http://irlab.daiict.ac.in/>)



- Speech Processing Lab (<https://sites.google.com/site/speechlabdaiict/>).

21. Special research laboratories sponsored by / created by industry or corporate bodies

A computational science lab comprising of a high performance computing cluster was set up by Reliance Communications to meet the needs of the newly introduced undergraduate program in computational science. This lab provides computing facilities required to build computational models and simulate them on advanced computing architectures.

22. Publications:

- * **Number of papers published in peer reviewed journals (national/international)**
- * **Monographs**
- * **Chapters in Books**
- * **Edited Books**
- * **Books with ISBN with details of publishers**
- * **Number listed in International Database (For e.g. Web of Science, Scopus, Humanities International Complete, Dare Database - International Social Sciences Directory, EBSCO host, etc.)**
- * **Citation Index – range / average**
- * **SNIP**
- * **SJR**
- * **Impact Factor – range /average**
- * **h-index**

Publication Type	2011	2012	2013	2014	2015
Number of papers published in peer reviewed journals	69	95	71	76	50
- National Journals and Conferences	15	17	18	9	7
- International Journals and Conferences	54	78	53	67	43
Books with ISBN with details of publishers	4	2	0	2	4
Chapters in Book	4	8	6	7	8
Books edited	2	0	2	0	1
Number listed in International Database (Scopus)	5	14	10	9	23
h-index	14				



23. Details of patents and income generated

Patents Awarded

- **Anjan Ghosh**– Remote cervical dilation monitoring system and method,” US Patent No. 8,100,840B2, dated 24 January 2012, University of Oklahoma.
- **Abhinay Pandya** – Medical Ontologies for Computer Assisted Clinical Decision Support," US Patent 2007/0094188 A1, December 09, 2009.
- **Suman Mitra** – A method for block based digital image watermarking, US Patent No.6707, 928, March 16, 2004.
- **Suman Mitra** – Method of compressing an image, US Patent No. 6738,520, May 18, 2004.

24. Areas of consultancy and income generated The faculty members have provided consulting services in the following broad areas: Renewable energy applications, hardware design, rural development, curriculum and content development.

The consultancies received during the last four years are listed below.

Sr. No	Organization	Title	Faculty	Year	Total Amount (in lakhs)	Consultancy Income (in lakhs)
1	IIT-Gandhinagar	Library system and processes	T. S. Kumar	2010	2,40,000/-	72,000/-
2	I-Nurture Education Solutions Private Limited	Animation Courseware	Binita Desai	2011	2,52,000/-	1,03,600/-
3	Adani Institute of Infrastructure Management	Renewable energy applications	Girja Sharan	2011	75,000/-	22,500/-
4	Marwadi Education Foundation	Electronics resource room for hands on experimentation	Rahul Dubey	2011	64,000/-	19,200/-
5	GIZ NABARD,	Brief Assessment:	Alka Parikh	2012	1,38,600/-	41,580/-



	Rural Financial Institution Programme	Natural Disaster Management, Role & Importance of Integrated Solutions in Risk Coping Against Future Calamities				
6	Ganpat University	FPGA - Design	Rahul Dubey	2012	25,000/-	7,500/-
7	Adani Institute of Infrastructure Management	Renewable energy applications	Girja Sharan	2013	90,000/-	27,000/-
8	USID Foundation	Design Challenge-2012	Asim Banerjee	2013	50,000/-	15,000/-
9	IIT Vadodara	Mentoring of new institute	DA-IICT	2014		10,11,560/-
10	Adani Institute of Infrastructure Management	Renewable energy applications	Girja Sharan	2014	90,000/-	27,000/-
						13,46,940

25. Faculty selected nationally internationally to visit other laboratories/institutions /industries in India and abroad

Institute encourages research visits by faculty to national and international institutes for collaborative research

Sr.No	Faculty	Host	Country	Period
1	Alka Parikh	Eurasian Center for food security	Tajikistan, Russia	May – Jul, 2015
2	V Sunitha	IMSc Chennai	India	May – Jun, 2014; May – Jul, 2013
3	Jaideep Mulherkar	University of California at Davis	USA	Jun, 2012
4	Laxminarayan Pillutla	IIT-Bombay, EE Dept.	India	May – Jun, 2012
5	Sourish Dasgupta	University of Missouri at Kansas City	USA	May – Jun, 2012



26. Faculty serving in

- a) National committees b) International committees

No. of faculty serving on steering/program committees of international conferences:

Year	No. of faculty
2012	20
2013	13
2014	12
2015	14
Total	92

- b) Editorial Boards

No. of faculty serving on editorial boards:

Year	National	International	Total
2012	0	5	5
2013	0	9	9
2014	1	8	9
2015	0	5	5
Total			38

- c) any other (please specify)

Sr.No.	Faculty Name	Position
1	Nagaraj Ramrao	<ol style="list-style-type: none"> Member, Academic Advisory Body, Gujarat Power Engineering & Research Institute. (Ex-officio) Member, Academic Council, Ganpat University, Mehsana, (Ex-officio) Member, Working Committee of drafting Gujarat Science, Technology & Innovation (STI) Policy, Govt. of Gujarat, Gandhinagar, (Ex-officio) Member, Executive Committee, Institute of Seismological Research, DST, Gandhinagar. (Ex-Officio) In-charge Director, IIIT Vadodara .(Ex-Officio) Independent Director, – Board of Directors, Gujarat Informatics Ltd., Member, Expert Committee for selection of Dr. Vikram A. Sarabhai Award, Gujarat Council on Science and Technology (GUJCOST)
2	Aditi Nath Sarkar	<ol style="list-style-type: none"> Member, Governing Council, Satyajit Ray Film & Television Institute, Kolkata Member, Governing Council, Satyajit Ray Film & Television Institute (SRFTI) Kolkata; ongoing.



Sr.No.	Faculty Name	Position
3	Alka Parikh	<ol style="list-style-type: none"> 1. Joint Director, Sarvang Utkarsh, A Micro Health Insurance Mutuals Organization working for the Slum Dwellers in Pune and Mumbai 2. Member, Annual General Meeting (AGM), Utthan, Ahmedabad 3. Member, Planning Commission's Working Group on Disaster Management-12th Five Year Plan, Government of India, 2011.
4	Amit Bhatt	<ol style="list-style-type: none"> 1. Member, Working Group on Innovation, International Telecommunication Union (ITU)
5	Anish Mathuria	<ol style="list-style-type: none"> 1. Expert Reviewer, Development Innovation Ventures, US Agency for International Development (USAID), 2012 2. Member, DeitY Committee, 'e-Authentication Standards,' Ministry of Communications and Information Technology, Government of India. 3. Member, Faculty Selection Committee, Institute of Infrastructure, Technology, Research and Management (IITRAM), Ahmedabad. 4. Member, PRSG (Project Review and Steering Group), DIT, Govt. of India PhD Thesis Examiner (External), IIT Guwahati
6	Anjan Ghosh	<ol style="list-style-type: none"> 1. Chapter Chair (Educational Activities), IEEE Gujarat 2. Member, Board of Studies (Electronics and Communication Engineering), Nirma University, Ahmedabad,
7	Asim Banerjee	<ol style="list-style-type: none"> 1. Member, Faculty of Engineering, Computer Science and Engineering, UV Patel College of Engineering, Ganpat University, Mehsana, Gujarat. 2. Member, Board of Studies, Nirma University, Ahmedabad 3. Member, Board of Studies, UV Patel College of Engineering, Ganpat University, Mehsana, Gujarat. 4. Senior Member, Institute of Electrical and Electronics Engineers (IEEE)
8	B. N. Hiremath	<ol style="list-style-type: none"> 1. Member, Advisory Board, Jaipur Rugs Foundation, Jaipur, Rajasthan 2. Member, Board of Governors, Jaipur Rugs Foundation, Jaipur.
9	Bhaskar Chaudhury	<ol style="list-style-type: none"> 1. Member, Institute of Electrical and Electronics Engineers (IEEE) 2. Member, LXcat Team, The Plasma Data Exchange Project 3. Member, Plasma Science Society of India (PSSI)
10	Deepak Ghodgaonkar	<ol style="list-style-type: none"> 1. Chair, Chapter Coordinator, IEEE Gujarat Section 2. Examiner, ME, Gujarat Technological University, Ahmedabad, July 2012 3. Fellow, The Institution of Engineers, Malaysia (FIEM), Malaysia 4. Life Fellow, Institution of Electronics and Telecommunication Engineers, (FIETE), India 5. Senior Member, Institute of Electrical and Electronics Engineers, (SMIEEE), USA. 6. Vice-Chair, IEEE Antennas and Propagation Society and Microwave Theory and Techniques Society, Joint Chapter, IEEE Gujarat Section, 2010.
11	Ganesh Devy	<ol style="list-style-type: none"> 1. Chairperson, People's Linguistic Survey of India, Bhasha Research and Publication Centre 2. Advisor, South Asia Board, Aide et Action, Paris. 3. Member, Committee of Living Cultural Traditions, Ministry of Culture, Government of India. 4. Member, Committee on Indigenous Languages and Special

Sr.No.	Faculty Name	Position
		<p>Study Centre, University Grants Commission.</p> <p>5. Member, Round Table on Indigenous Knowledge and Endangered Languages, Ministry of Human Resource Development, Government of India.</p> <p>6. Mentor, Bhasha Research and Publication Centre, Baroda.</p>
12	Girja Sharan	<p>1. Designed and installed a community solar cooker unit at SEWA office Ganeshpura, Mahesana, February 2013</p> <p>2. Member, Indian Council of Agricultural Research, 2013-16</p> <p>3. Member, Research Advisory Committee of Central Arid Zone Research Institute (CAZRI), Jodhpur</p> <p>4. Member, Research Advisory Committee of Central Research Institute for Dry land Agriculture (CRIDA), Hyderabad</p>
13	Hemant A. Patil	<p>1. Life Member, System Society of India (SSI)</p> <p>2. Member, International Association for Engineers (IAENG)</p> <p>3. Member, International Speech Communication Association</p>
14	Manjunath V. Joshi	<p>1. Co-Chair, Program, Third Asian Conference on Computer Vision Workshop on E-Heritage, 2014</p> <p>2. Member, Program Committee, Asian Conference on Computer Vision, 2014</p> <p>3. Member, Expert Committee, National Board of Accreditation (</p>
15	Mazad Zaveri	<p>1. Member, Executive Committee, Network of Engineering Institutions, Gandhinagar, Gujarat</p>
16	Mehul Raval	<p>1. Advisor, Telecommunication Sector Skill development Council (TSSC), New Delhi, June 2013</p> <p>2. Elected as IEEE Senior member for Contribution towards the IEEE and Technical Domain, IEEE, April 2013</p> <p>3. Member, Board of Studies (Computer Engineering), R K University, Rajkot, Gujarat.</p> <p>4. Member, Board of Studies (Electronics, Computer Engineering), Veer Narmad South Gujarat University, Surat</p> <p>5. Member, IT- T working group on Innovation, ITU (International Telecommunication Union), Geneva, January 2012</p> <p>6. Secretary, IEEE Gujarat Section</p> <p>7. Student Activity Chair, IEEE Gujarat Section.</p> <p>8. Vice Chair, IEEE Gujarat Section</p>
17	Prabhat Ranjan	<p>1. Panelist, Workshop on Challenges and Solutions in Bridging the GAP of Skilled HR in ESDM, Dept of Information Technology, Govt of India, Delhi, March 2012</p> <p>2. Panelist, Strengthening Educational and Training Institutions, Global Summit on Changing Bihar : Forging Partnerships for Development, Patna, February 2012</p>
18	Radha Parikh	<p>1. Member, Advisory Committee, ENVIS, Center of Excellence in Environment Education</p> <p>2. Board Member, Rabbani Educational Trust, Gwalior</p>
19	Rahul Dubey	<p>1. Chair, IEEE chapter of Industry Applications/Industrial Electronics/Power Electronics, Gujarat Section, IEEE, Gujarat, 1 August 2013 to 31 July 2014</p> <p>2. Educational Activity Chair, IEEE Gujarat Section, IEEE, Gujarat, 1 August 2013 to 31 July 2014</p> <p>3. Member, Board of Studies of Instrumentation and Control Engineering (Degree Program), Nirma University, Gujarat, 1 August 2013 to 31 July 2014</p> <p>4. Chair, IEEE Gujarat Section Chapter and Joint Chapter of Industrial Applications Society, Industrial Electronics and</p>

Sr.No.	Faculty Name	Position
		<p>Power Electronics Society.</p> <ol style="list-style-type: none"> Member, Board of Studies, Instrumentation and Control Engineering, Nirma University, Ahmedabad. Member, Expert Committee, Sardar Sarovar Canal Automation Project
20	Rutu Parekh	<ol style="list-style-type: none"> Session Chair, Signal Processing and VLSI, INDICON 2013, Impact of Engineering on Global Sustainability, 13-15 December, IIT Bombay, Mumbai. As a member of editorial board for Journal of Electrical and Electronic Engineering, Science Publishing Group journal, from Sept. 2013. http://www.sciencepublishinggroup.com/journal/editorialboard.aspx?journalid=239 Member of International Scientific Committee for 2014 International conference on Artificial Intelligence and Communication Engineering (AICE2014) held on May 20-21, 2014 in Macao Member of International Scientific Committee, International Conference on Communication Technology and Application (CTA2014) held on August 19-20, 2014, in Beijing, China
21	S. C. Sahasrabudhe	<ol style="list-style-type: none"> Chairman, Peer Committee, Visiting Team for Evaluation of the Institutes, National Board of Accreditation (NBA). Director, Gujarat Venture Finance Ltd., Ahmedabad, Gujarat Director, Sahajanand Laser Technology Limited, Gandhinagar, Gujarat Director-In-Charge, IIIT Vadodara External Expert, Recruitment and Assessment Centre (RAC), DRDO. Member, Board of Directors, Sahajanand Laser Technology Ltd., Gandhinagar. Member, Board of Governors, Consumer Education & Research Society, Ahmedabad Member, Board of Governors, IIT Mandi, Himachal Pradesh Member, Board of Governors, Vishwakarma Institute of Information Technology, (VIIT), Pune Member, Board of Governors, Visvesvaraya National Institute of Technology (VNIT), Nagpur Member, Board of Management, Yashwantrao Chavan College of Engineering (YCCE), Nagpur. Member, Core Committee, AICTE, New Delhi. Member, Core Committee, Bharti Airtel IITD Center for Excellence in Telecommunication, IIT Delhi Member, Executive Committee Team for Washington Accord, National Board of Accreditation (NBA), New Delhi Member, Executive Council, Central University of Gujarat Member, Governing Council, Electronics and Quality Development Centre, (EQDC) Govt. of Gujarat. Member, Governing Council, Sardar Patel Institute of Technology (SPIT), Mumbai Member, Working Group on Technology Development for Indian Languages (TDIL) Programme, Dept. of Information Technology, Govt. of India, New Dlehi Special Invitee, Indian Engineering Dean's Council, Indian Chapter of GEDC of Indian Society for Technical Education, (ISTE), Bengaluru and Sivakasi, Tamil Nadu.

Sr.No.	Faculty Name	Position
22	Sanjay Chaudhary	<ol style="list-style-type: none"> 1. Chair, Computer Society Chapter, IEEE Gujarat, 2010-11. 2. Reviewer, Project Proposals, NRDMs, Dept of Science and Technology, Govt of India
23	Sanjeev Gupta	<ol style="list-style-type: none"> 1. Coordinator, Research & Academic Activities, Space Applications Centre (ISRO, Dept of Space, Govt. of India), Ahmedabad 2. External Expert, Departmental Promotion Committee (DPC), Scientist/Engineer SC/SD, Space Applications Centre (ISRO, Dept of Space, Govt. of India), Ahmedabad 3. External Expert, Faculty Selection Committee, Institute of Technology, Nirma University, Ahmedabad 4. External Expert, Promotion Interview Committee (Scientist-SB to Scientist-SC and Scientist-SC to Scientist-SD), Space Applications Centre (SAC), Department of Space, Government of India, Ahmedabad. 5. Member of "Antenna Test and Measurement Association" (ATMA). 6. Member of Academic Council of CU Shah University, Wadhwan City, Surendranagar District, Gujarat. 7. Member of Agilent's Test Advisory Panel. 8. Member of Board of Governors (BOG) of Government Engineering College (GEC), Gandhinagar. 9. Member of Board of Studies (BOS), Indus University, Ahmedabad. 10. Member of Board of Studies (BOS), LDRP Institute of Technology and Research, Kadi Sarva Vishwa vidyalaya, Gandhinagar 11. Member, Board of Studies, Indus University, Ahmedabad.
24	Satish Deshpande	<ol style="list-style-type: none"> 1. Executive Committee Member, ADINET (Ahmedabad Library Network) 2. Member, Working Group on Census, Content Creation & Community Information Centres: National Mission for Libraries (Ministry of Culture, Govt of India) 3. Executive Committee Member, ADINET (Ahmedabad Library Network) 4. Member, Working Group on Census, Content Creation & Community Information Centres : National Mission for Libraries (Ministry of Culture, Govt of India)
25	Suman K. Mitra	<ol style="list-style-type: none"> 1. Member, Academic Committee, Indian Institute of Information Technology (IIIT), Vadodara, Gujarat. 2. Secretary, IEEE Gujarat Section 2011. 3. Chair, IEEE Gujarat Section, India
26	T. S. Kumbar	<ol style="list-style-type: none"> 1. Panel Member to review and recommend research proposals for "2011 Indian LIS Research
27	Tridip Suhrud	<ol style="list-style-type: none"> 1. Honorary Director, Adivasi Academy, Tejgadh. 2. Member, Standing Committee, Sabarmati Ashram Memorial and Preservation Trust, Ahmedabad.
28	V Sunitha	<ol style="list-style-type: none"> 1. Member, Academy of Discrete Mathematics and Applications, Mysore, Karnataka, India, 2012 2. Member, Board of Studies (Mathematics), Pandit Deendayal Petroleum University, Gandhinagar, Gujarat.
29	Vijaykumar Chakka	<ol style="list-style-type: none"> 1. Member, Academic Council and Board of Studies, Ganpat University, Mehsana

Sr.No.	Faculty Name	Position
30		<ol style="list-style-type: none">1. (Hon.) Director, Andaman and Nicobar Tribal Research Institute (ANTRI)2. Member and Research Advisor on Tribal affairs. Andaman and Nicobar Administration3. Member, Tribal Welfare Policy Research, National Advisory Council New Delhi and Planning Commission New Delhi, on the position of 'Particularly Vulnerable Tribal Groups' (PVTGs) India4. Expert, Tribal Welfare of the Andaman and Nicobar Islands, Govt of India

27. Faculty recharging strategies (UGC, ASC, Refresher/orientation programs, workshops, training programs and similar programs).

Faculty at DA-IICT are self motivated. They have devised various innovations in teaching and learning both technically as well as pedagogically. In addition faculty attend various summer schools, workshops, seminars, conferences etc., so as to hone their teaching skills. The Institute provides financial support to faculty members and research scholars towards registration fee and travel expenditure to attend these refresher programmes both in India and abroad. The funds may be used for attending training and research programs for professional development as well. The Institute regularly organizes conferences, seminars and workshops for promoting interactions with wider academic and research community.

28. Student projects

All our programmes have a strong component of project work to fulfil the academic requirement of the programmes. The B.Tech programme requires student to do a final year project of duration of at least 13 weeks, either split into two semesters or as a full semester project. The M.Tech programme requires student to do a year-long thesis work. The M.Sc (IT), M.Sc (ICT in ARD) and M.Des (CD) programmes require student to undertake the final semester project work.



- percentage of students who have done in-house projects including inter-departmental projects

Programme Name	%age of students who have done in-house projects
B.Tech (ICT)	60
M.Tech (ICT)	100
M.Sc (IT)	10
M.Sc (ICT in ARD)	10
M.Des (CD)	100

- Percentage of students doing projects in collaboration with other universities/ industry/ institute

Programme Name	%age of students who have done off-campus projects
B. Tech (ICT)	40
M.Tech (ICT)	0*
M.Sc (IT)	90
M.Sc (ICT in ARD)	90
M.Des (CD)	0*

- *M.Tech and M.Des curriculum requires students to do their research/project on campus.

Sr. No.	Faculty	Recognition	Agency	Year
1	Sanjay Srivastava	Senior Member Grade	IEEE	2015
2	Ganesh Devy	Padmashree	Govt. of India	2014
3	Asim Banerjee	Senior Member Grade	IEEE	2013
4	Mehul Raval	Senior Member Grade	IEEE	2013
5	Mehul Raval	Asia Pacific Outstanding Branch Counselor Award	IEEE RIO	2012
6	Prabhat Ranjan	Bihar Gaurav Samman	Bihar Govt.	2012
7	Manik Lal Das	Senior Member Grade	IEEE	2012
8	Vijay Chakka	Senior Member Grade	IEEE	2012
9	Ranendu Ghosh	Team Excellence Award	ISRO	2012
10	Sanjay Choudhary	Literary Award	Gujarat Sahitya Academy	2012
11	Ganesh Devy	Linguapax Award	Linguapax Institute	2011
12	Tridip Surud	Sahitya Academy Award	Sahitya Academy, New Delhi	2010
13	Suman Mitra	Senior Member Grade	IEEE	2008
14	M V Joshi	Dr. Vikram Sarabhai Award	Gujarat Council of Science & Technology	2007

29. Awards/recognitions received at the national and international level by Faculty

Doctoral / post doctoral fellows / Students

Sr. No.	PhD students	Recognition	Agency	Year
1	Shah Milind Siddharthbhai	INSPIRE fellowship	DST, Govt. of India, New Delhi.	2011
2	Khaja Ahmad Shaik	TCS Research Fellowship.	TCS	2011
3	Ashish Phophalia	The Best Paper Award in “National Conference on Future Trends in Information and Communication Technology and Applications (NCICT-2011)”	IEEE	2011
4	Shubham Jain	Was selected for fully sponsored Research Internship at Swedish Institute of Computer Science (SICS), Stockholm, Sweden during Summer 2012. He was also selected for “Network Science School in Electrical Engineering and Computer Science” organized by IISc, Bangalore from 2-6 January 2012 and “TCS Excellence in Computer Science Week (TECS	TRDDC	2012



Sr. No.	PhD students	Recognition	Agency	Year
		Week 2012)''		
5	Shrishail S. Gajbhar	10,000 INR as prize for best paper award on "Image Denoising using Redundant Finer Directional Wavelet Transform" at the Fourth National Conference on Computer Vision, Pattern Recognition, Image Processing and Graphics (NCVPRIPG), 2013	Indian Institute of Technology, Jodhpur (IIT-J).	2014
6	Nirmesh J. Shah	800 USD as travel grant from IEEE Signal Processing Society (SPS) to attend and present paper in IEEE International Conference on Acoustics, Speech and Signal Processing (ICASSP), 2014	Florence, Italy	2014
7	Nirmesh J Shah	Bhavik Vachhani and Hardik, "Effectiveness of PLP-based Phonetic Segmentation Algorithms for Speech Synthesis", <i>Proceedings of International Conference on Acoustics, Speech and Signal Processing (ICASSP)</i> ,	Florence, Italy, pp. 270-274,	2014
8	Shalin Shah and Dixita Limbachiya	Presented a research paper titled "DNACloud: A Tool to Store Big Data on DNA" at Foundations of Nanoscience Conferenc (FNANO'14) organised at Snowbird, Utah, USA	Duke university	2014
9	Vinod Mall	Received the "President Police Medal for Distinguished Service"	Government of India	2014
10	Sanket S. Patel	"Design & Analysis of Low Noise Amplifier at Ku-Band", this paper stood First in the All India Student Paper Contest (Doctoral category)	IEEE M V Chauhan Award for the Doctoral Research in India by IEEE India Council; and awarded at MVCPC 2014, Pune, India,	2014
11	Parth Mehta	Travel Grant from DAAD to attend Autumn School in 2015 for Information Retrieval & Information Foraging (ASIRF)	DAAD	2015
12	Anshu Chittora	Best paper award during	ICBAPS 2015	2015

Sr. No.	PhD students	Recognition	Agency	Year
		ICBAPS 2015, Malaysia		

M Tech

Sr. No.	Students	Recognition	Agency	year
1	Gitam Shikkenaw	Google Indian Women in Engineering Award 2011	Google	2011
2	Vivek Goswami	Visiting Students' Research Programme (VSRP 2010) Scholarship by School of Technology & Computer Science, Tata Institute of Fundamental Research (TIFR), Mumbai, India.	TIFR	2011
3	Yash Soni	USID Gurukul conducted by USID Foundation (India) at IITK on 26 th August to 4 th September 2011	IITK	2011
4	Akash Kamthan	A student of M Des 2009 batch, has received Best Student Documentary Award for “Dekha Andheki: Kaal Aur Kala” on the block printers of Sanganer; the documentary has been selected for screening at the 8 th Jeevika: Asia Livelihood Documentary Film Festival 2011	Centre for Civil Society, New Delhi.	2011
5	Kishan Patel	Sponsored internship at Carnegie Mellon University (CMU) in May-July 2010. He worked on the project playpower (playpower.org) on HCI (Human Computer Interaction) aspect of Computer Science under Prof. Matthew Kam and Derek Lomas.	Carnegie Mellon University (CMU)	2011
6	Aditya Bhatt, Aakriti Gupta, Parth Gupta, Swair Shah, Viranch Mehta and Siddharth Kothari	Selected for the prestigious Google Summer of Code (GSoc) Internship from 25 May to 22 August 2011	Google	2011
7	Vivek Goswami	Selected as Research Intern at the School of Technology on Computing Science, Tata Institute of Fundamental Research, Mumbai from January to May 2011. He was also selected to attend the 2011 Summer School on Security and Privacy. He was also an invited reviewer for	Microsoft Research India, Bengaluru	2011

Sr. No.	Students	Recognition	Agency	year
		Software Quality Journal published by Springer.		
8	IEEE Student Branch	Women in Engineering (WIE) wing of the IEEE Student Branch at DA-IICT has been selected to win the first place as the 2010 IEEE WIE Affinity Group of the Year Award for the Asia-Pacific Region	IEEE	2011
9	Ankita Mehta	3 rd year BTech student, has bagged the “Outstanding Woman in Engineering Award” conferred by Women in Engineering (WIE) Affinity Group of IEEE Student Branch, in association with IEEE Gujarat Section and IEEE Region 10 (Asia and Pacific) WIE. She is also serving as the Google Student Ambassador	IEEE	2012
10	Megha Tak	Final year BTech student, was chosen as the “Outstanding Chapter Chair” among the 150 student chapters from across the globe	IEEE	2012
11	Mohit Setia and Megha Tak	Final year BTech students, were awarded first prize among 19 countries for their outstanding performance at a global competition organized at the IEEE Industry Application Society (IAS) Annual Meeting 2011 held at Las Vegas, Nevada, USA from 7-11 October 2011. They were awarded for their vibrant video on Indian Culture and Tourism titled “India – Our Motherland”. Each of them won \$1,400 under the prestigious Zucker Travel Award given by IEEE Education Department for academic and overall excellence	IEEE	2012
12	Mohit Setia and Megha Tak	Final year BTech students, were also fully funded to represent Industry Application Society (IAS) Student Chapter from India and present paper titled “Converting Waste heat from Automobiles to Electrical Energy” at International Power Electronics and Motion Control Conference	Industry Application Society	2012



Sr. No.	Students	Recognition	Agency	year
		(IPEMC-2012) at Harbin, China in 2-5 June 2012		
13	Sidharth Kothari	Selected as one of the 350 mentors, mentored high school students with Tux4Kids during 2011 Google Code	Google	2012
12	Mohit Setia and Megha Tak	Final year BTech students, were also fully funded to represent Industry Application Society (IAS) Student Chapter from India and present paper titled "Converting Waste heat from Automobiles to Electrical Energy" at International Power Electronics and Motion Control Conference (IPEMC-2012) at Harbin, China in 2-5 June 2012	Industry Application Society	2012
13	Sidharth Kothari	Selected as one of the 350 mentors, mentored high school students with Tux4Kids during 2011 Google Code	Google	2012
14	Sonali Dubey and Yash Soni	Final year BTech students, were selected among the top seven in the country for "Microsoft Imagine Cup" (as a part of software design competition by Microsoft) to assist and improve the exercise of patients suffering from motor disorder, post stroke syndromes and cerebral palsy	Microsoft	2012
15	Yash Shah (first year), Saheb Motiani, Neel Shah, Smit Sanghavi, Rajat Gupta (from second year), Brijesh Patel, Smit Patel, Jigar Raisinghani, Aditya BSRK, Shivaraman Aiyer, Jiten Thakkar, Pankaj Bhambhani, Nityam Vakil, Jainit Purohit (from third year) and Viranch Mehta, Anirudh Subramanian along with Gaurav Arora	Seventeen students of DA-IICT were selected from 4,285 applicants from over 100 countries across the world for the prestigious "Google Summer of Code 2012 internship". These interns worked on different projects under GSoC for three months from 21 May and each got a stipend of \$5,000.	GSoC	2012
16	Maulik C. Madhavi	IAPR (International Association for Pattern Recognition) Travel	International Conference on	2012



Sr. No.	Students	Recognition	Agency	year
		Scholarship for presenting a joint paper, "Significance of magnitude and phase information via VTEO for humming based biometrics,"	Biometrics, ICB' 12, Delhi, 30 March-1 April 2012.	
17	Raghuvir Songhela	Summer Research Fellowship Programme (SRFP-2012) ,As a part of it, he did his research internship at IIT Delhi in the field of Computer Networks.	Indian Academy of Sciences (IAS), Bangalore	2012
18	Ankita Mehta,	3rd year BTech student, worked as Research and Development Intern	Carnegie Mellon University.	2012
19	Yash Shah	Invited to attend KDE Akademy in Estonia, Europe in July 2012	KDE	2012
20	Student Group	DA-IICT Google Developers' Group (GDG) is selected as one of the 271 approved Google Developers Group chapters in 87 countries around the world.	Google	2012
21	DA-IICT-WIE wing	Women in Engineering (WIE) wing of the IEEE Student Branch at DA-IICT won the '2010 IEEE WIE Affinity Group of the Year Award" for the Asia-Pacific Region in September 2011.	IEEE	2012
22	IEEE Student Branch	IEEE Student Branch of DA-IICT won 2nd position in "Innovative Student Branch Competition" at IEEE All India Student Congress 2012 held at Bangalore	IEEE	2012
23	IEEE Student Branch	IEEE Student Branch of DA-IICT won Best Student Branch 2011 award given by IEEE Gujarat section.	IEEE	2012
24	IEEE-IA Society	IAS (Industry Applications Society) Student Branch was formed at DA-IICT in December 2011, with a starting branch fund of \$500.	Industry Applications Society	2012
25	Ankita Mehta,	Student won the "Outstanding Woman in Engineering (WIE) Award 2012" conferred by WIE Affinity Group of IEEE Student Branch,She is also serving as the Google Student Ambassador.	Nirma University in association with IEEE Gujarat Section and IEEE Region 10 (Asia and Pacific)	2013
26	Ankita Mehta,	Selected as the scholarship recipient from all over India by the Anita Borg Institute for	Anita Borg Institute for Women and	2013



Sr. No.	Students	Recognition	Agency	year
		Women and Technology to attend the Grace Hopper Conference 2012 to celebrate Women in Computing from December 12-14, 2012 at Bangalore.	Technology	
27	Ankita Mehta,	Was selected among Top 16 Google Student Ambassadors (GSAs) for the term 2012 – 2013 from a total of 123 GSA's across India	Google	2013
28	Ankita Gupta,	Won 3rd Prize at Startup Weekend Ahmedabad held at IIM-Ahmedabad for pitching an idea called Event Flow	IIM-Ahmedabad	2013
29	Ankita Mehta, Sharan Shodhan and Om Thakkar	147th in the ACM-ICPC 2012, Asia Region, Onsite Contest and Ranked 219th ACM-ICPC Online Coding Contest held in December 2012.	ACM	2013
30	Abhinav Tripathi, Anuj Kosambi, Jaydeep Solanki and Somsubhra Bairi	Judged second runner up in Microsoft Imagine Cup under World Citizenship category for India finals held at Hyderabad on April 10, 2013. The team developed an app for Windows Phone 8 platform that lets a blind person read at his own without any help from anyone else and received a cash prize of INR 60,000 and each team member also got a Nokia Lumia 920.	Microsoft Imagine Cup	2013
31	Nagendra Chowdary,	Judged runner up in the western region in the first edition of the IET India Scholarship Award – Regional Rounds that took place in Pune on July 20, 2013 at College of Engineering Pune. . In the third and final round, the participants made their presentations on the topic 'Technological Solutions for Effective Water Management'. He received cash prize of INR 20,000.	IET India	2013
32	Mayank Ladha	Semi-finalist in Ahmedabad Mirror Talent Hunt – 2013.	Times Group	2013
33	Zeel Shah and Raj Buddhdev	Won 1st prize in 2013 Industry Application Society's (IAS) Web Chapter Contest.	Industry Application Society's (IAS)	2013
34	Vaibhavi H Desai	First prize in "Women in Industry	Industry	2013

Sr. No.	Students	Recognition	Agency	year
		Awareness Challenge" organised by Industry Application Society(IAS) and Woman in Engineering (WIE) chapters of DA-IICT.	Application Society(IAS) and Woman in Engineering (WIE)	
35	Student Group	Seventeen DA-IICT students have been selected for the Google Summer of Code (GSoC)-2013 contest from over 100 countries across the world for the prestigious "Google Summer of Code 2012 internship". These interns worked on different projects under GSoC for three-month home-based internship and each got a stipend of \$5,000	Google	2013
36	Saksham Gupta and Brijesh Patel	The prestigious Myron Zucker Travel Award given by the IEEE Industrial Application Society (IAS) for academic and overall excellence. The award will enable them to attend the IEEE Industry Application Society (IAS) Annual Meeting 2013 to be held at Orlando Florida, USA from October 6, 2013.	IEEE	2013
37	Megha Tak	IAS SB DA-ICT in the Applied Power Electronics Conference (APEC) held in USA in March 2013	IAS	2013
38	IEEE-IA Society	A grant of \$500 was awarded for the best presentation in 2nd All Indian IAS workshop in TSEC Mumbai in April 2013. Two 2 projects on ADICCS and sickle cell Anaemia each got a grant of \$500 for execution of the project	IAS	2013
39	IEEE Student Branch	DA-IICT was also awarded the Outstanding IEEE Student Branch Chapter Award 2013 as well as the first prize in IEEE Industrial Application Society's (IAS) Web Chapter Contest	IEEE	2013
40	Kesha Shah	selected as one of the five finalist for Women in Open Source by Red Hat internationally in Academic Award category	Red Hat	2014
41	Saksham Gupta and Brijesh Patel	The prestigious Myron Zucker Travel Award given by the IEEE Industrial Application Society (IAS) for academic and overall	IEEE	2014



Sr. No.	Students	Recognition	Agency	year
		excellence. The award enabled them to attend the IEEE Industry Application Society (IAS) Annual Meeting 2013 held at Orlando, Florida, USA from October 6, 2013.		
42	Vaibhavi Desai,	Student won Google Student Ambassador, DA-IICT (Year 2013-14), Google Summer of Code 2014 student at Systers, an Anita Borg Institute (May - August 2014) and Google Anita Borg Memorial Asia-Pacific Scholarship (July 2014).	Google	2014
43	Kesha Shah	Scholar for Google Anita Borg Memorial Scholarship Asia-Pacific 2014. She is also a Co-founder, Women Who Code (WWC) - Gujarat, a global non-profit which empowers women to pursue careers in technology, advocates for gender diversification in work place and expands career opportunities for women in technology sector. She was also selected as Mentor for Google Code-In contest to introduce pre-university students (age groups 13-17) to the many kinds of contributions that make open source software development possible.	Google	2014
44	Zeel Shah and Raj Buddhdev	1st prize in 2013 Industry Application Society's (IAS) Web Chapter Contest, in which global institutes participated.	Industry Application Society's (IAS)	2014
45	Yash Shah,	Was invited by Google to attend largest developer conference Google I/O held on June 25-16, 2014 in San Francisco. He was also invited by Ryerson Futures and Ryerson University to Toronto in May 2014 to experience the market opportunities there. He was also invited to give a talk at FOSSASIA 2014 in Cambodia in February 2014. He was also invited to give a talk at Akademy 2013 in Spain, which is the annual world summit of KDE, one of the	Google	2014



Sr. No.	Students	Recognition	Agency	year
		largest Free Software communities in the world. He also attended Mozilla Summit 2013, California (Santa Clara), US where he was invited on the basis of his contribution to Mozilla Open Source		
46	Jineet Doshi,	Won the IEEE Student Enterprise award 2013. He also won the "Outstanding Volunteer Award" from the IEEE Gujarat Section. He received a grant of \$1000 for his project "ICT-based Solutions for Education in Rural India". The project involves teaching under-privileged kids in remote areas of the country through a network of tablets connected to the internet through MiFi devices. It involves establishing the necessary hardware infrastructure followed by content delivery through custom interactive apps, animated videos and web services. The proposed model is highly scalable, self sustaining, easy to implement and inexpensive.	IEEE	2014
47	Jineet Doshi,	Research paper "ICT-based Solutions for Education in Rural India - A Case Study" has been selected for presentation at the IEEE GHTC (Global Humanitarian Technology Conference) 2014 to be held in Silicon Valley, California.	IEEE	2014
48	Priyansh Trivedi, Yash Shah	Ranked #1 by Google Developers Group (GDG) in India in 2014 in terms of number of events organized in last 6 months.	Google Developers Group (GDG)	2014
49	Mayank Hinger, Manan Dhawan and Arkit Vora	Won the first prize at IIT Bombay Techfest (held on January 3-5, 2014) in the event "Appsurd" for developing an android application. They received Certificates and an Acer Iconia W5 tablet convertible to laptop.	IIT Bombay Techfest	2014
50	Aditi Bhatnagar	Selected as Google Student Ambassador from DAIICT for the year 2013-14.	Google	2014
51	Saurabh Patel,	Was selected in Google Summer of Code 2014 (May 19 – August	Google	2014



Sr. No.	Students	Recognition	Agency	year
		18, 2014).		
52	Hardik Avaiya	First Position in XENESIS - 2014, a National Level Symposium in the event X-Treme organized by LDRP-IRT, Gandhinagar on March 27, 2014.	LDRP-IRT, Gandhinagar	2014
53	Smit Kotadiya and Nigam Shah	The winners in IT Quiz held at Rollwala College, Ahmedabad.	Rollwala College, Ahmedabad	2014
54	Smit Kotadiya	Received appreciation letters for vulnerability reporting from Sony, SPOJ Email Brain, Almamater, Ferrari, Hacker Rank, Hacker Earth and skyscanner	Sony	2014
55	Abhishek Shukla	Received selected for the grant of Short Service Commission in the flying branch of Indian Air Force; his training will commence at Air Force Academy, Dundigal, Hyderabad on July 7, 2014.	Indian Air Force	2014
56	IEEE Student Branch	DA-IICT was also awarded the Outstanding IEEE Student Branch Chapter Award 2013 as well as the first prize in IEEE Industrial Application Society's (IAS) Web Chapter Contest.	IEEE	2014
57	Nidhi Vyas	Did her summer internship under Prof. Jenniffer Mankoff at Carnegie Mellon University, USA from May 4 to July 24, 2015. She was paid \$1859 by the University.	Carnegie Mellon University, USA	2014
58	Kshitij Sharma	Was selected Google Summer of Code (GSOC) 2014; he did a project titled "Web Driver System Tests for Joomla CMS" at an organization "Joomla!.	Google Summer of Code	2014
59	Ganesh Iyer	Received selected for Google Summer of Code 2014 with Emory University as his mentoring organization for his project on "Biomedical Data Exploration and Visualization".	Google Summer of Code	2014
60	Shalin Shah and Vijay Dhameliya	Presented a research paper titled "ImPatho - An Image Processing based Pathological Decision Support System for Disease Detection and an Novel Tool for Overall Health Governance" at IEEE R10 HTC 2014 conference.	IEEE	2014

Sr. No.	Students	Recognition	Agency	year
61	Shikhar Kumar Gupta and Foram Meghal Joshi	Were invited to the Foundations of Nanoscience Conference (FNANO' 14) organised at Snowbird, Utah, USA by the Duke university on April 11-14, 2014 to present their research work on a software 3DNA (a tool for DNA sculpting).	Duke university	2014
62	Kesha Shah	Won Red Hat Women in Open Source Award 2015	Red Hat	2015
63	Student Group	11 BTech students of DA-IICT selected for Google Summer of Code 2015.	Google	2015
64	Pramod B. Bachhav	Travel Grant from IEEE Signal Processing Society Travel Grant to present research paper in ICASSP 2015	IEEE	2015

30. Seminars/ Conferences/Workshops organized and the source of funding (national / international) with details of outstanding participants, if any.

The Institute regularly organizes conferences, seminars and workshops for promoting interactions with wider academic and research community.

Institute organized the following national and international conferences during the last four years.

Sr.No	Title	Source of funding	Dates
1	FIRE 2015	DeITY, Google, Yahoo	4-6 December, 2015
2	TENSYMP 2015	IEEE, Guj Cost	13-15 May, 2015
3	WiSSAP 2015	DeITY, DRDO, ISCA, Guj Cost	4-7 January, 2015
4	National Workshop on Cyber Security	Guj Cost, GSFC	16-17 November, 2013
5	Workshop on Graph and Geometric Algorithms	NBHM	10-12 March, 2012



Outstanding participants during various events organised

Events	Year	Name of the outstanding participants	Affiliation
FIRE 2015	2015	Charles L. A. Clarke	University of Waterloo, Canada
		Doug Oard	University of Maryland, USA
		Gareth Jones	Dublin City University, Ireland
		Jaap Kamps	University of Amsterdam, Netherlands
		Kareem Darwish	Qatar Computing Research Institute
		Paulo Quaresma	University de Evora, Portugal
		TENSYMP 2015	2015
Miwako Doi	National Institute of Information and Communications Technology, Japan		
Vijay Ansari	Ohio Research Scholars Endowed Chair, University of Dayton, USA		
Mohan Kumar	Phillips, India		
Santosh Madathil	Wipro		
Siby Abhraham	Wipro		
Ravinder Dahiya	Sensors Council		
Kavitha Laxmi	SAP Labs		
Sashank Jain	SAP Labs		
Vinod Desai	ARM India		
WiSSAP 2015	2015		
		Shihab Shamma	University of Maryland at College Park, USA
		Hynek Hermansky	Johns Hopkins University, USA
		Jaap Kamps	University of Amsterdam, Netherlands
		Kareem Darwish	Qatar Computing Research Institute
		Paulo Quaresma	University de Evora, Portugal
		Pushpak Bhattacharyya	Director, IIT Patna
		National Workshop on Cyber Security	2013
Amit Kumar	CEO, Bio Axis DNA Research Center		
Manan Thakker	Cyber Lawyer		
Manish Naik	Information Security Consultant		
Nadkumar Saravade	Independent Security Consultant		
Nina Godbole	Independent Consultant		
Rajesh Deo	Network Intelligence India Pvt. Ltd.		
Vicky Shah	Independent Security Consultant		
Workshop on Graph & Geometric Algorithms	2012	Srikrishnan Divakaran	DAICT
		Daya Gaur	IIT Ropar
		Abhiram Ranade	IIT Bombay
		Sachin Patkar	IIT Bombay



Events	Year	Name of the outstanding participants	Affiliation
		Niranjan Balachnadrans	IIT Bombay
		Ashok Amin	DAIICT
		Sathish Govindarajan	IISc Bangalore
		Subir Kumar Ghosh	TIFR Bombay
		Sudebkumar Pal	IIT Karagpur
		Subodh Kumar	IIT Delhi
		Sharat Chandran	IIT Bombay
		Amitabha Mukerjee	IIT Kanpur

Below is a listing of the workshops and training programs that have been held at the Institute in the last four years.

Sr.No	Title	Dates	Sponsors
1	Workshop on ICT for Development	25-Jul-15	Ministry of Earth Sciences
2	DAIICT-TCS Workshop Series II	17-Jul-15	DAIICT
3	Workshop on Intellectual Property Rights	11-Jul-15	TIFAC
4	Workshop on Bio inspired Computing	22-24 June 2015	ACM, RCOM
5	BHUVAN – A Geo-spatial Geo-portal Services	8-May-15	ISRO
6	DAIICT-TCS Workshop Series I	10-Apr-15	DAIICT
7	CRC Press Editorial Workshop	21-Aug-14	CRC Press
8	DAIICT-SAC Brainstorming Workshop	14-May-13	DAIICT
9	Using Open Access Resources for Professional Development	16-Feb-13	ADINET (Ahmedabad Library Network)
10	NPTEL Awareness Workshop	15-Jan-13	Classele
11	Workshop on Image Super-Resolution	24-25 August 2012	SAC-ISRO
12	NEI Workshop on Design of CMOS Analog Circuits	11-22 June 2012	DAIICT
13	Basics of Geomatics Using Open Source Software	28 May to 1 June 2012	DAIICT

31. Code of ethics for research followed by the departments

Being a research led teaching Institute, DAIICT takes the pride of those faculty members, technical staffs and a large section of under graduate and post graduate students take part in active research. DAIICT attracts considerably large amount towards funding of various projects conceived by the faculty members. The quality maintained is quite high and meeting national and international standards. The code of ethics for research as mentioned below provides an articulation of the values and principles underlying the institute's research investigation methodologies. This code of ethics is expected to be a source of supports and confidence to the faculty members, a reassurance to all staffs participating in research projects and an indicator of quality to those who use and consult the corresponding research reports.

Role and Responsibilities

- Protect the dignity and wellbeing of self and all stakeholders such as research participants, collaborators, funding agencies and the institute.
- Avoid exploiting personal relations, rather stick to the high professional ethics.
- Be sensitive to the issues and problems of society while framing the research problems, collecting information, conducting experiments and interpreting results and findings.
- Maintain and keep all the records arising out of the research to handle queries that are to be answered in appropriate manner for future.
- Discuss results of research only for professional purpose, and only with those who are clearly entitled to know or be consulted.

Promoting High Standards

- Ensure that highest quality and standards are maintained in problem framing, experiments and reporting of results.
- Do not compromise with the quality of research and the outcomes that provides undue advantage to any individual or group of individuals.
- Utmost care should be taken while reporting results such that nothing can be misinterpreted and misused against the development of society at large.
- All parties involved should be informed in time for conflict of interest, if any.



Avoidance of Infringement

- Take care that there is no infringement of any published work while reporting research results.
- Avoiding plagiarism is must. Take utmost care against informed or uninformed plagiarism. Whenever and wherever necessary, try to get the consent from the appropriate authority. In case consent could not be received, appropriate acknowledgement should be made.
- Plagiarism check and an ethical review is must before publishing any document related to research.

Privacy and Confidentiality

- Explore and collect only that information which is appropriate to the purpose of a given investigation or intervention.
- Take care that there is no discomfort, while conducting the research, on the personally or culturally defined space of any person including stake holders and research participants unless clear and appropriate permission is granted to do so.
- Do not relay, except justified by law, confidential information about any stakeholders to which it has become privy in the course of research.
- Inform those to whom services are offered about legal limits on confidentiality where it is appropriate to do so.

Avoiding Harm

- Do not engage in research that promotes or intended to create discomfort to any individual or group of individuals.
- Avoid doing research that directly or indirectly cause harm to any species.
- Do not take up any issue of research that may affect the harmony of the society or causes problem towards integrity to any individual, group of individuals or as whole the society.
- Seek an independent and adequate ethical review of the balance of risk and potential benefit before taking up any research issue.

32. Student profile programme-wise:



Applications and Selection

Programme	Applications Received				Selected Students							
	2011	2012	2013	2014	2011		2012		2013		2014	
					Male	Female	Male	Female	Male	Female	Male	Female
B.Tech (ICT)	22601	18051	6561	8495	192	51	188	55	221	69	240	59
M.Tech(ICT)	1079	1305	919	941	44	6	41	9	33	17	39	14
M.Sc(IT)	1252	1112	783	541	51	39	61	29	62	28	52	28
M Sc (ICT-ARD)	34	34	63	32	10	0	5	4	5	5	5	2
M.Des (CD)	50	38	26	37	7	4	2	4	3	4	3	3
PhD	99	149	105	97	11	4	9	7	7	1	3	3

Pass Percentage

Programme	% of Pass							
	2011*		2012#		2013\$		2014^	
	Male	Female	Male	Female	Male	Female	Male	Female
B.Tech (ICT)	98.99	100	98.75	100	96.89	100	94.35	100
M.Tech (ICT)	100	100	96.43	100	97.67	100	87.18	100
M.Sc (IT)	100	100	100	100	100	100	98.31	100
M Sc (ICT-ARD)	100	100	100	100	90	-	100	100
M Des (CD)	100	100	-	-	100	100	100	100
PhD	100	-	100	100	-	-	100	-

*Graduating students of UG 2007 (and backlog 2006, 2005). PG 2009 and 2008

#Graduating students of UG 2008 (and backlog 2007, 2006). PG 2010 and 2009

\$Graduating students of UG 2009 (and backlog 2008, 2007). PG 2011 and 2010

^Graduating students of UG 2010 (and backlog 2009, 2008). PG 2012 and 2011



33. Diversity of students

Program	2011				2012				2013				2014			
	% students from Same University	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries	% students from Same University	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries	% students from Same University	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries	% students from Same University	% of students from other universities within the State	% of students from universities outside the State	% of students from other countries
B Tech	-	49.79	48.97	1.23	-	48.97	50.21	0.82	-	63.45	35.86	0.69	-	72.91	26.09	1
M Tech (ICT)	-	54	46	-	-	50	50	-	-	50	50	-	-	43.4	56.6	-
M Sc (IT)	-	87.78	12.22	-	-	88.89	11.11	-	-	78.89	21.11	-	-	97.5	15	-
M Sc (ICT-ARD)	-	50	50	-	-	11.11	88.89	-	-	70	30	-	-	33.33	66.67	-
M Des (CID)	-	27.27	72.73	-	-	33.33	66.67	-	-	57.14	42.86	-	-	42.86	57.14	-
Ph D	26.67	40	33.33	-	18.75	68.75	12.5	-	25	66.67	8.33	-	16.67	33.33	50	-
M Des (CD)	-	3	8	-	-	2	4	-	-	4	3	-	-	3	4	-
Ph D	4	6	5	-	3	11	2	-	3	8	1	-	1	2	3	-

34. How many students have cleared Civil Services and Defense Services examinations, NET, SET, GATE and other competitive examinations? Give details category-wise?

Precise data is not available. However, through interaction with student community and placement cell, it is estimated that at least 15 percent of the graduating students go for further studies. Out of the candidates going for further studies, around 60 percent go for management courses and the others apply for MS/M.Tech/PhD degrees both within the country and outside, and so must have taken NET / SAT / GATE /CAT / GRE / TOFEL / GMAT). Very few opt for Civil Services and Defense Services examinations. DA-IICT Alumni web portal (<https://daiict.almaconnect.com/>) will allow us to better track the student profile after graduation.

35. Student progression

Student progression	Percentage against enrolled
UG to PG	15
PG to M.Phil.	-
PG to Ph.D.	10
Ph.D. to Post-Doctoral	50
Employed	
Campus selection	81
Other than campus recruitment	2
Entrepreneurs	1

36. Diversity of staff

Percentage of faculty who are graduates*	Number	%age
of the same university	1	2
from other universities within the State	5	10
from universities from other States	24	48
from universities outside the country	20	40
Total	50	100

*The qualifying degree for faculty is PhD



37. Number of faculty who were awarded M.Phil., Ph.D., D.Sc. and D.Litt. during the assessment period

None

38. Present details of departmental infrastructural facilities with regard to

a) Library

Library follows an open access system where users can walk in to library and directly access resources. The Library operations are fully computerized and connected to campus network and the users can access all the online and digital resources.

Sr. No.	Particulars	No. of items
1.	Air-Conditioning of the Periodicals Hall	Yes
2.	Air-Conditioning of the Digital Resources Centre	Yes
3.	IBM Server 7945 ICS for Hosting SLIM LAS and NPTEL Video Courses with 3 TB HDD	01
4.	Desktops	05
5.	High-Brightness Multimedia Projector	01
6.	Up Gradation of Servers for IR and OPAC by Adding 4 GB RAM and 600 GB HDD each (Total 6 GB RAM and 700 GB HDD)	02
7.	CD Mirroring Server	01
8.	Up Gradation of six PCs Allotted to Staff and 16 Users PCs with Latest Hardware	22
9.	PoE Switch for WiFi Access Points and Three WiFi Access Points	03
10.	Bluray Disc Player	01
11.	Headphone	08
12.	Laser Printer	02
13.	Advanced Auto Detect Barcode Scanner	02
14.	Manual Barcode Scanner	02
15.	External HDD with 1 TB Capacity for Backup	01
16.	Digital Repository for Hosting Scholarly Content using D-Space	01
17.	Security Camera	08
18.	E-book Reader “infibeam Pi”	01



Sr. No.	Particulars	No. of items
19.	NPTEL Video Courses in 500 GB Hard Disks	03
20.	Study Cubicle	43
21.	Computer Chair	41
22.	Chairs for the Reading Room	91
23.	Audio-Visual Table	07
24.	Cupboards for Storing Audio-Visual Materials	06
25.	Display Rack for New Arrivals Books	01
26.	Display Rack for New Arrivals AV Materials	01
27.	Display Rack for New Arrivals Journal Issues	02
28.	Umbrella Stand	01
29.	Raincoat Stand	01
30.	Temperature Monitoring Device	01
31.	Display Rack for Newspapers	02
32.	Rack for Bound Volumes	06
33.	Wall mounted Poster Display Stand	04
34.	Display Rack for Faculty Publications & Awards	02
35.	Notice Board	02
36.	Wooden Cabinet for Posters & Maps	01
37.	Steel Shelves for Books	01
38.	File Cabinet	05

b) Internet facilities for staff and students

The institute has high speed (1Gbps) Internet connectivity through National Knowledge Network, subscription is for 10 years since 2010 at the cost of 50 lacs. The campus is also WiFi enabled and students' hostel rooms are connected with high speed Internet.



c) Total number of class rooms

Details of ICT enabled classrooms & seminar rooms

Room description (Number)	Usage	Shared/ Exclusive	Capacity	Facilities available
Classrooms (15)	Classroom/ Tutorial	Exclusive	1300	PC, Internet, Projector, Document Camera, Audio System
Seminar rooms (2)	Workshop & Training	Exclusive	50	PC, Internet, Projector
Conference rooms (1)	Seminars & Faculty Meetings	Exclusive	65	PC, Internet, Projector, Audio System, Wi-Fi
Lecture Theatres (3)	Classroom Seminar/ Workshops	Exclusive	850	PC, Internet, Backlit projection system, Document camera, Audio System

Learning space at Laboratory is utilized to perform experiments on a day to day basis with ICT enabled laboratory rooms, also used to conduct workshops, to conduct seminars and placement activity.



Details of Laboratory Space

Lab Room no.	Signage / Usage	Approx. size (feet)	Seating capacity	Remarks
1	001- Desktop Computer Based Lab / To perform software based course practical	48'x28'	65	Teaching lab
2	002 - Desktop Computer Based Lab / To perform software based course practical	48'x28'	92	Teaching lab
3	003 - Language Lab / Language lab to enhance overall communication by use of software and Desktops Computer with headphone/mike	15'x22'	21	Teaching lab
4	004- Desktop Computer Based Lab / To perform software based course practical	31'x43'	66	Teaching lab
5	005 - Desktop Computer Based Lab / To perform software based course practical	31'x43'	66	Teaching lab
7	007 - Desktop Computer Based Lab / To perform software based course practical	48'x28'	66	Teaching lab
8	008 - Desktop Computer Based Lab / To perform software based course practical	48'x28'	66	Teaching lab
11	011- Desktop Computer Based Lab for General Lab usage	48'x29'	64	Teaching lab
101	101 - Electronics Lab / To perform software/hardware based course practical. Lab contains various Testing and Measuring instruments & kits as well as Desktops	48'x28'	64	Teaching lab
102	102 - Electronics Lab / Network lab To perform software/ hardware based course practical. Lab contains Testing and Measuring instruments & kits as well as desktops & networking devices	48'x28'	64	Teaching lab
104	104 - Electronics Lab / To perform software/hardware based course practical. Lab contains various Testing and Measuring instruments & kits as well as desktops	59' x 31.8'	60	Teaching lab
105	105 - Project Lab / provided to students those who wants to use their own laptop	28.5' x 39.1'	40	Teaching lab – cum – project lab
107	107 - Electronics Lab / To perform software/hardware based course practical. Lab has Desktops, various Testing and Measuring instruments & kits.	48' x 28'	60	Teaching lab
108	108 - Digital Signal Processing Lab / To perform To perform software/ hardware based course practical with use of various DSP kits, workspace allotted to sponsored project with desktops, also having a workspace for M Tech students	48' x 28'	56	Teaching lab – cum – PG students workspace
110	110 - RF Lab / To perform software/hardware based course practical with use of various Testing and Measuring instruments ,	48' x 28'	60	Teaching lab –cum – PG /Phd student



Lab Room no.	Signage / Usage	Approx. size (feet)	Seating capacity	Remarks
	workspace allotted to PhD students with desktop, provision for using student's own laptop			workspace
201	201 - PG Lab / workspace allotted to M Tech 1st and 2nd years students with desktop as well as with their own laptop	48' x 28.5'	70	M Tech student workspace
202	202 - Research Lab / Workspace allotted to PhD students with desktop as well as sponsored project / research labs, provision for using student's own laptop	48' x 28'	27	Research lab – cum PhD student workspace
203	203 - VLSI Lab / workspace allotted to M Tech students of the VLSI group where students can use their own laptop.	15'x22'	10	VLSI – cum –VLSI student workspace
204	204 - M.Sc. (ICT in Agriculture and Rural Development) lab with Desktops as well as the provision made for students those who want to use their own laptop also / Project lab - where provision has been made for students to use their own laptops also.	40'x31'	50	Teaching lab – cum – laptop user workspace
205	205 - VLSI Lab / VLSI course lab as well as workspace allotted to with desktop M Tech students belong to VLSI group	31.8' x 43.8'	60	Teaching lab – cum – VLSI student workspace
206	206 - Research Lab / Workspace allotted to M Tech students belongs to Magnet group as well as course lab for elective subject, provision for using student's own laptop	24'x14.5'	10	Research lab – cum – M Tech student workspace
207	207 -Functional lab for Computational Science.	48'x28'	64	Proposed teaching lab
208	208 - Research Lab / Workspace allotted with desktop to PhD students as well as sponsored project / research labs, provision for using student's own laptop	48'x28'	25	Research lab – cum PhD student workspace
211	211 - Project Lab / Elective lab for robotics, wireless sensor network, Topics in Medical electronics, project lab, provision for using student's own laptop as well as having desktops and instruments / kits related to lab activity	48'x29'	64	Teaching lab – cum – project lab
213	213 - Research Lab / workspace allotted to M Tech students with desktop belongs to Distributed Computing / Virtualization / Cloud Computing, provision for using student's own laptop	20' x 13.9'	7	Research lab – cum – M Tech student workspace

Sr.No.	Description	No.	Capacity
1	Lecture Theatres	1	250 students
2	Lecture Theatres	2	600 students
3	Class Room	2	180 students
4	Class Room	3	120 students
5	Class Room	5	80 students
6	Class room	4	40 students
7	Spare rooms	2	120 students
8	Spare rooms	2	30 students
9	Multimedia Studio	2	MDes Programme

d) Class rooms with ICT facility

- All Lecture Theatres/Class rooms are ICT enabled and following is the ICT equipped present in each classroom.
- Desktop Computer with Internet connectivity
- Overhead Projector
- Document camera
- Whiteboard
- Writing Pad
- Wireless Mike
- Sound system
- Laser pointer presenter
- Desk based students seating in gallery system

e) Students' laboratories

The laboratory building houses state-of-the-art teaching and research laboratories for electronics, communications, computers and networks. Computers are installed with 1Gbps backbone network connectivity at the laboratory building. Students use resources of the laboratory to perform experiments for various courses and work on projects guided by faculty. The labs provide extensive research facilities due to their classifications and specialization on the basis of the research to be carried out as well as to carry out special events and workshops organized by students and placement



activity. The laboratory building is enabled with WI-FI and every laboratory room has intercom facility.

The institute has a standing Information and Communication Technology (ICT) committee, comprising with faculty members and senior system personnel, who look after all requirements of software and hardware in laboratories related to academic processes. Below is a brief overview of various labs, categories, equipment and capacity.

Computer Labs

There are 7 Computer Labs dedicated to various courses in Computer and Communication Technology. The labs facilitates programming/software practical by students with help of teaching assistant and laboratory assistant for various courses - application software, system software, computer programming tools. The labs are equipped with the following licensed software and various open source software.

ADS - Advance Design System for RF, Microwave & Signal integrity - ADS2003A, ADS2005A, ADS209; ArcGIS - Version 9.0; Adobe Creative Suite 5.5 Design Premium; Adobe Creative Cloud for Teams; Autocad 2005; Cadence Version 7.1; Globarena - English Language; IBM websphere; IGIS - Ver1.1; KEIL MDK, Version 5.9; LabVIEW; LTSpice; MATLAB-2010B & 2011A; Multisim; Mind Manager; Oracle Academy 2015; QualNet; Rational Rose suite enterprise; SPSS; WaveFormer Pro v15.0; Xilinx ISE 10.1; Xilinx ISE Vivado System Edition; ZeBu-UF Fast ASIC Emulator.

There is extensive use of Open source software/firmware in the labs that include Open source OS Fedora (having in-built various packages, tools as well as software), Eagle 6.5.0--- light edition, Scilab, Miktex, LT Spice/swCAD III, Winrar, Acrobat reader, VLC player, open office, Turbo C, Pspice student version, Wireshark, Apache Tomcat, Eclipse, Eclipse with UML tool Object Aid, Jbuilder, Jcreator LE, Mozilla Firefox browser, Google chrome browser, MYSQL, Net beans, PG Admin –III, PHP, Python, Shockwave player, SSH secure shell client, bloodshed devc++, LC3 simulator, Java with java doc, Arduino, QGIS, CMAP, Vensim, MicroImage TMT, Postgresql,, DIA, Silos, Crimson Editor, Logisim, Applian FLV Player, Clamwin Free Antivirus , AVR studio, Edit plus, GPL ghost script, KEIL Vision 4,



OMNET++, StarUML, Winpcap, 8085 simulator, Magic VLSI layout, kchmviewer, php Designer, NS-2, SUMO.

Network Lab

The Network lab facilitates practice hours of Computer Networks course. The equipment available in Network lab includes: LAN Trainer kit & Manual; Compact Wireless-G USB adapter; DWL-120+; L2- switch - DES-3226S; L3- switch - DES-3326S; Wireless access point- DWL-900AP+; IBM 16/4 Token-Ring PCI Management Adapter; Multi-station access unit -- MAU 8228 plus; D-link 8-port unmanaged switch - DES-1008V; Xbee explorer USB board; ArduinoXbee Shield board; EDUP make 802.11n USB wireless Nano adapter; 7-port USB hub; Zigbee USB interfacing board and several Open source simulators/tools.

VLSI Laboratory

Sr. No.	Equipment	Configuration
1	Workstations	37 P4 Intel workstations with Linux RHEL 4 & Windows XP. 3 SUN SPARC servers with 21" monitors
2	EDA tools	Cadence Designer suite; RTL compiler; SOC Encounter; Spectre; Virtuoso Layout Designer; NC; Verilog; Verilog XL; Verilog AMS based designing; Mentor Graphics Designer Suite; FPGA Advantage; Modelsim Simulator; Eldo; Xelga; Calibre; IC Flow; Time-it; Seamless FPGA; Formal Pro; Design For Test; Xilinx design suite; Xilinx ISE; Chipscope Pro; System Generator; Xilinx EDK.
3	Hardware and Software Co-simulator	EVE ZeBu-UF Fast ASIC Emulator
4	Hardware Design Kits	65 Xilinx SPARTAN-3 based DSP boards; 20 FPGA development boards; ProAsic Plus Starter Kit; Altera Development Kits; Xilinx AFX PQ 240-100 Proto Board; NI ELVIS II: Educational Laboratory Virtual Instrumentation Suite II; Field Programmable Gate Array Cyclone(Embedded System Development Kit)

VLSI lab facilitates experiments on low-power processor architectures, low power circuit design techniques, analog and mixed-signal circuit design, RF device modeling and RF circuit design, Rapid prototyping of Digital Systems, Industrial Automation, reconfigurable processors, Digital Arithmetic, Advanced Processor Architectures, VLSI



Implementation of Speech and Image processing algorithms, Biomedical signal processor design, testing and formal verification, memory design, Embedded VLSI, asynchronous circuits, CAD tools, Graph theory and Optimisation problems in VLSI.

RF Communication Lab

The software and instrumentation in the RF Lab comprises a state-of-the-art facility for design and analysis of modern wireless circuits and systems. The RF laboratory is equipped with the following equipment:

Sr. No.	Equipment	Configuration
1	RF Signal Generator	100 kHz to 3 GHz, -100 to + 10 dBm
2	Spectrum Analyzer	9 kHz to 3 GHz, -114 to + 30 dBm
3	Vector Network Analyzer	300 kHz to 8.5 GHz, -15 to +10 dBm, including 2-Port S-Parameter Test Set with Time Domain Analysis Capability and Type N Calibration Kit
4	Single Channel Power Meter	10 MHz to 18 GHz, -30 dBm to +20 dBm
5	Analog Oscilloscope	100 MHz
6	Microstrip Systems Trainer	0.85 to 1.3 GHz with Microstrip Analysis Software for Spectrum / Network Analysis
7	Agilent ADS simulation tool	Agilent Technologies Advanced Design System (ADS) simulation tool

Electronics Lab

There are 4 Electronics Lab used for teaching courses like Basic Electronics Circuits, Computer Networks, Analog Circuits, Embedded Hardware Design, Analog and Digital Communication, Computer Organization, Digital Logic Design, Computer Organization, Digital Logic Design, Introduction to Communication System and electives courses in the domain. The following are the equipment available in these labs.



Sr.No.	Category	Equipment
1	General Electronics	Analog Oscilloscope; Advanced TechLab ST223; Advanced TechLab ST225; Caddo – 803; Analog Starter Kit; ASLK2010V2.0; CNC Milling machine; Model 3D Plotter; Digital Oscilloscope; Tektronix TDS210; RIGOL - DS1102C; Tektronix TDS1002C-EDU; Digilent Electronics Explorer Board; Digital Lab Trainer Kit; Abvolt Digital trainer kit; Scientech ST2611; Data Logger; DrDAQ; Data acquisition device; NI MyDAQ; Function Generator; Tektronix CFG253; Tektronix CFG280; Caddo 4065; Caddo 4065.rev; LCR Meter; Caddo 9305; Microprocessor Trainer Kit; Dyna85; Multimeter; Caddo - 61T; Scientific SM7022; NI ELVIS II; ELVIS II; Power Supply; DC o/p; Triple o/p ; Fixed o/p; Regulated DC power supply; Universal IC Tester; Caddo 9352
2	Medical Electronics	Heartbeat pulse sensor with Analog out; ECG sensor; Blood pressure/Heart Rate sensor with Display+ Analog out; EEG Machine
3	Embedded and Robotics	Atmel500 STK kit; Single board computer; PSoc - CY3209 kit; PSoc - CY3210 kit; NetFPGA; LabView Robotics sbRIO starter kit; ATAVRDRAGON JTAG debugger; ATAVRXPLAIN Evaluation kit for ATXMEGA128A1; ATEVK1100 AT32UC3A0 series evaluation kit; Arduinocompatible - Freeduino DU; LPC1769 LPCXpresso board user manual and Schematic diagram; Easy-AVR6and Ready for AVR kit; Line array sensor, Side wall sensor, IR sensor transceiver, Sharp distance sensor; Ultrasonic distance sensor; AVR STK600; Raspberry PI-Model A with camera board; Raspberry PI Model-B; Beagle Bone Black; Intel Galileo Development Board; Raspberry PI 2; Mini RTC module for PI; BMP180 barometric pressure sensor ; XTRINSC-SENSE-Board , BH1750FVI light sensor

Digital Signal Processing Lab

To perform software by use of desktops as well as hardware base practical by B Tech students with use of various DSP kits, workspace also allotted to M Tech & sponsored project with desktops. The lab is equipped with the following accessories:

Xtreme DSP development Kit ; ARM7 - Embedded GSM Development Board ; ADSP-2181 EZ-Kit Lite; eZdsp TSM320LF2407; ARM7 - Embedded Bluetooth Development Board; ADSP-21061 EZ-Kit Lite; Innovator Development kit with OMAP platform; NI SPEEDY-33; ADSP-21161 Ez-Kit Lite; XDS560 TMDSEMU560; COMBLOCK boards 3505 / 3504 /1500-A / 1005 / 5102; ADSP-2189M EZ-KIT Lite; ADSP-218X EZ-ICE.



Sr. No.	Laboratory Name	Laboratory Room	Configuration	No. of Computers
1	001-Computer Lab	1	Intel core 2 1.866Ghz / 2GB RAM / 160GB HDD	65
2	002-Computer Lab	2	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	92
3	003-Language Lab	3	Intel Pentium IV 1.70Ghz / 512MB RAM / 40GB HDD	20
			Intel Pentium-IV 3.066Ghz / 1GB RAM / 80GB HDD	1
4	004-Computer Lab	4	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	63
			P-4 2.00GHz / 512MB RAM / 40GB HDD/ CD Drive	3
5	005-Computer Lab	5	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	63
			P-4 2.00GHz / 512MB RAM / 40GB HDD/ CD Drive	4
6	006-Staff Room-1/ Printer Room	6	Laser Printer	1
7	007-Computer Lab	7	Intel Pentium IV 2.60Ghz / 512MB RAM/ 40GB HDD / CD Drive	66
8	008-Computer Lab	8	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	63
9	009-Electrical Room-1	9	Houses Electrical Control Panels, Switch gear, centralized power control equipment	-
10	010-Security Room-1	10	- Space for Security Guards	-
11	011-Computer Lab	11	Intel Pentium IV 2.66Ghz / 1.5GB RAM / 80GB HDD	34
			Intel Pentium IV 3.00Ghz / 1.5GB RAM / 80GB HDD	30
12	012-Server Room - 1	12	Mac Pro G5 Apple Workstation : Intel Xeon 2.66GHz Quad core, 16GB RAM, 740GB/3*1TB HDD with onboard Graphic card	1
			Intel XeonE5-2640, Intel chipset C602, RAM 16*2 ECC1600,HDD 1TB 7200rpm,VGA G200, NIC intel i350 Gigabit	4
			Intel Xeon 2*E5-2620 2GHZ/16GBRAM/4*600GB SAS HDDD	1



Sr. No.	Laboratory Name	Laboratory Room	Configuration	No. of Computers
			Intel Xeon E5-2420v2(2.2GHz/6-core/15MB, Turbo2), 12 DiMM Slots, 32 GB Ram, 8 SFF (2.5inch) Hot Plug SAS/SATA, 2*300GB 6G SAS 10K rpm SFF (2.5-inch) HDD, 4*900GB 6G SAS 10K rpm SFF HDD	1
			Intel Xeon 4C e5530 2.4Ghz, 4*4,8*4= 48GB RAM, RPSU, 4*300GB SAS HDD, Raid Controller	2
			Intel P-4 2GHz and above, 2GB RAM, 160GB HDD, Raid-5	1
			2*Intel Xeon 3.0GHz, 8GB RAM, 2*146GB/3*300GB SCSI HDD	1
			2*Intel Xeon 2.6GHz/7.5GB RAM/2*36GB SCSI HDD/2*300GB SCSI HDD	1
			2 Blade (HS2EA3 BLADE 7875B1A)/2*48GB RAM/ 4*300GB SAS HDD/6*600gb SAS/ 1*2/4 ETHERNET CARD	1
			Intel Core2Duo,3GB RAM, HDD 1TB/500GB	1
13	101-Electronics Lab	101	Intel Pentium IV 2.00Ghz / 1GB RAM / 40GB HDD	26
			Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	5
14	102-Electronics Lab/ Network Lab	102	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	48
15	103-Staff Room	103	Space for staff	-
16	104-Electronics Lab	104	Intel Core2Duo 3.00Ghz / 1GBRAM /160GBHDD	4
			Intel dual core 1.60Ghz / 1GB RAM / 80 GB HDD	28
17	105-Project Lab	105	Intel P-IV 1.80GHz / 512MB/ 40 GB HDD	15
18	106-Store	106	Storage facility	-
19	107-Electronics Lab	107	Intel Pentium IV 2.6Ghz / 1GB RAM / 40GB HDD	28
			Intel Pentium IV 2.6Ghz / 768MB RAM / 40GB	1

Sr. No.	Laboratory Name	Laboratory Room	Configuration	No. of Computers
			Intel Pentium IV 2.6Ghz / 512MB RAM / 40GB HDD	1
20	108-Digital Signal Processing Lab	108	AMD Athlon 64bit Processor 3200+ / 4GB RAM / 160GB HDD	4
			Intel Pentium-IV 1.80Ghz / 1GB RAM / 40Gb HDD	7
			Intel Pentium D 2.66Ghz / 1GB RAM / 80GB RAM	12
			Intel Pentium D 3.00Ghz / 1GB RAM / 160GB HDD	1
			Intel P-IV 2.66Ghz / 1GB RAM / 80GB HDD	5
			Intel Pentium-IV 3.00Ghz / 1GB RAM / 80GB RAM	11
			Intel Pentium-IV 1.80Ghz / 512MB RAM / 40GB HDD	1
			Intel Pentium-IV 3.06Ghz / 2GB RAM / 80GB HDD	1
21	109-Electrical Room -2	109	- Houses Electrical Control Panels, Switch gear, centralized power control equipment	-
22	110-RF Lab	110	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 8GB RAM / 500GB HDD	1
			Intel Core2Duo 2.8Ghz / 1GB RAM / 160 HDD	3
			Intel P-IV 3.06Hz / 1GB RAM / 80GB RAM	3
			Intel P-IV 1.7Ghz / 2GB RAM / 40GB HDD	1
			Intel P-IV 1.8Ghz / 1GB RAM / 40GB HDD	1
23	111-Help Desk -cum- Server Room -2	111	Intel XeonE5310 1.6Ghz Quod Core two CPU 28GB RAM 3*146 GB SAS, 4*500GB SAS	2
			IBM Tape Storage Model 3573, with 2slot	1
			Intel Core2Duo,3GB RAM, HDD 1TB/500GB	1
			Intel Core-i5, 24GB RAM, HDD 3TB/2*256GB	3
			Intel Core-i5, 16GB RAM, HDD 3TB/1TB	1
			Intel Core-i5, 4GB RAM, HDD 500GB/3TB	1
			Intel Core2Duo,4GB RAM, HDD 420 GB	1
24	201-PG Lab	201	Intel P-IV 2.60GHz / 1GB RAM / 40 GB HDD	31
25	202-Research	202	Intel Core2Duo 2.93Ghz / 1GB RAM / 250GB HDD	1

Sr. No.	Laboratory Name	Laboratory Room	Configuration	No. of Computers		
	Lab		Intel P-IV 1.70Ghz / 1GB RAM / 40G HDD	1		
			Intel P-IV 2.60Ghz / 1GB RAM / 40GB HDD	2		
			Intel P-IV 1.80Ghz / 1GB RAM / 40GB HDD	3		
			Intel P-IV 2.00Ghz / 1GB RAM / 40GB HDD	2		
			Intel core2Duo1.80Ghz / 2GB RAM / 80GB HDD	1		
			Intel Core2Duo / 3GB RAM / 240GB HDD	1		
			Intel P-IV 1.80Ghz / 1.5GB RAM / 40GB HDD	1		
26	203-VLSI Lab	203	Intel P-4 3.00GHz / 2GB RAM / 80 GB HDD	7		
			Intel Core i5 3rd Generation processor 3470 @3.20GHz / 8GB RAM / 500GB HDD	1		
			Intel Core2Duo / 4GB RAM / 160GB HDD	1		
27	204-MSc(ICT in ARD Lab) / Project Lab	204	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	10		
28	205-VLSI Lab	205	Intel P-4 3.00GHz / 2GB RAM / 80 GB HDD	28		
			Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	1		
			HP workstation HP XW6600 / Xeon E5205 1.86Ghz / 8GB RAM / 250GB HDD	1		
29	206-Research Lab	206	Intel Core2Duo 3.0Ghz / 160GB HDD / 4GB RAM	3		
			Intel Core2Duo 1.86GHz / 160GB HDD / 1.5GB RAM	3		
			Intel P-IV 2Ghz / 40GB HDD / 512MB RAM	1		
			Intel P-IV 1.7Ghz / 1GB RAM / 40GB HDD	1		
			Intel P-IV 1.8Ghz / 1GB RAM / 40GB HDD	1		
					Intel i-5 3.20Ghz / 8GB RAM 1TB HDD / NVIDIA GT730	30
			31	208-Research Lab	208	P-IV / 1GB RAM / 80GB HDD
			P-IV / 1GB RAM / 40GB HDD	1		

Sr. No.	Laboratory Name	Laboratory Room	Configuration	No. of Computers
			P-IV 1.80Ghz/ 786MB RAM / 40GB HDD	1
			P-IV 1.70Ghz / 1GB RAM / 80GB HDD	2
			p-IV 2.60Ghz / 512MB RAM / 80GB HDD	1
			P-IV 1.80Ghz / 1GB RAM / 40GB HDD	1
			P-IV 1.80Ghz / 1GB RAM / 80GB HDD	2
			P-IV 2.0Ghz / 1.5GB HDD / 80GB HDD	1
			Intel core2Duo 1.86Ghz / 1GB RAM / 160GB HDD	1
32	209- Electrical Room -3	209	Houses, Electrical Control Panels, Switch gear, centralized power control equipment	-
33	210- Security Room -2	210	Space for Security Guards	-
34	211-Project Lab	211	Intel Core i5 3rd Generation processor 3470 @3.20GHz / 4GB RAM / 500GB HDD	1
			Intel Core2Duo 3.00Ghz / 2GB RAM / 160 HDD	2
			Intel Core2Duo 3.00Ghz / 1GB RAM / 160 HDD	15
			Intel P-IV 1.80Ghz / 1GB RAM / 40GB HDD	12
35	212-Server Room -3	212	Houses Network Server, Mail Server, Campus Server, and all equipment require to administer campus ICT facility.	-
36	213- Research Lab	213	Intel core2Duo 1.86Ghz / 512MB RAM / 160GB HDD	1
			Intel core2Duo 1.86Ghz / 3GB RAM / 160GB HDD	2
37	Workshop & Basic Sciences Lab	CEP 204A/B	Carpentry tools, hand tools. Physics Lab Equipment	30 capacity

Language Lab

English Lab is designed to enhance overall communication skills amongst students including pronunciation, accent, etc. to make them successful in careers. The Lab is a fully computer-based lab. The lab is setup by use of Multimedia enabled desktop PCs with network facility to access lingua-phone s/w as well as Globarena s/w which is installed locally on 15 nos. of PCs at the lab.

ICT in Agriculture and Rural Development Lab

The objective of the Lab is to prepare students who will be able to design systems based on information and communication technology and integrate these in farming operations, rural businesses and services. The practical component of courses like taught in the Lab: Computer basicsIT655 Information System Modeling, Quantitative Analysis – I& II, Management Information Systems, ICT Infrastructure Implementation and Applications, Modeling and Simulation of Dynamic Systems, e-Governance for Development, Precision Farming, Remote Sensing and GIS, Systems Approaches to Sustain Dev. The following equipment are available in the lab:

Infrared Thermometer; Lux Meter; 1-Wire Pressure Sensor; 1 – Wire Weather Instrument (AAG Model TAI8515; One-wire system plugs into a spare USB port; Garmin iQueM5; Rain Guage; 1 Wire Humidity Sensor; Juno SB Handheld; Wired Rain meter.

The list of hardware resources is as follows:

Desktop PC

In addition to the above mentioned information, there are a few research laboratories (refer to the next section (e)) primarily made for sponsored projects.

Desktop Net FPGA

The NetFPGA platform contains one large Xilinx Virtex2-Pro 50 FPGA which is programmed with user-defined logic and has a core clock that runs at 125MHz. The NetFPGA platform also contains one small Xilinx Spartan II FPGA holding the logic that implements the control logic for the PCI interface to the host processor.



ZeBu Workstation

ZeBu-UF Fast ASIC Emulator hosted on HP workstation, which having Intel Xeon E5205 1.86 6MB/1066 DC CPU-1, 8GB(4x2GB) DDR2-667 ECC FBD RAM, 250GB SATA 3GB NXQ 7200 1” HDD, 16x DVD+RW supermulti SATA 1’ Drive, no floppy disk option, NVIDIA quardo FX370 256MB PCIe Graphics, USB standard keyboard, USB optical scroll mouse, red hat linux WS 5, 64bit OS HP LP2275w 22” wide screen LCD monitor configuration with add on PCI card(called ZeBu-UF Ultrafast emulator) from M/S Eve design automation.

Audio-Visual equipment

Amplifier; Projector Overhead; Document camera/visualizer; Digital Note recorder; Projector-Slide; Wireless Presenter; Mike; Mixer; Player-DVD; Tape Recorder; VCR; AV Receiver; Projector-DLP; VGA Switcher; Projector-LCD; Digital Video Camera Recorder / Digital still camera.

GPU enabled systems, total 4 servers, each consists of: NVIDIA GPU cards (Tesla k40, GTX 680, GTX 690), Intel Xeon E5-2640 v2 (Sixteen-core, 20M cache, 2.0 GHz, 7.2 GT/sec), 32GB DDR3 RAM, 1TB HDD. CUDA compiler, nVIDIA CUDA toolkit, OpenMP, MPI and MPICH.

a. Research laboratories

Location	Signage / Usage	Approx. size (feet)	Seating capacity	Remarks
Lab building Room 202	202 - Research Lab / Workspace allotted to PhD students with desktop as well as sponsored project / research labs, provision for using student's own laptop	48' x 28'	27	Research lab – cum PhD student workspace
Lab building Room 208	208 - Research Lab / Workspace allotted with desktop to PhD students as well as sponsored project / research labs, provision for using student's own laptop	48' x 28'	25	Research lab – cum PhD student workspace
CEP building Room 003	Workspace allotted for research activities / sponsored projects	1370 Sq. Ft.	50	Information Retrieval and Language Processing Lab
CEP building Room 006	Workspace allotted for research activities / sponsored projects	760 Sq. Ft.	30	Speech Research Lab
CEP building Room 008	Workspace allotted for research activities	530 Sq. Ft.	20	Knowledge Discovery and Management Lab
CEP Building Room 208	Workspace allotted for research activities/sponsored projects	530 sq. ft.	20	Hydroponics LAB

39. List of doctoral, post-doctoral students and Research Associates

a) from the host institution/university

b) from other institutions/universities

Doctoral Students

Sr no	Name	Year of registration
1	Shah Jalpa Bharatkumar	2011
2	Maulik Chandulal Madhavi	2011
3	V. Ram Naresh Kumar	2011
4	Sanket Sureshbhai Patel	2011
5	Nileshkumar Vaishnav	2011
6	Patel Rashmit Kumar	2011
7	Vasavada Tejas Mukeshbhai	2011
8	Shalini A Rankawat	2011
9	Sarita Agrawal	2011
10	Chaudhari Payal Devendrabhai	2011
11	Padalkar Milind Gajanan Sunit	2011
12	Shrishail Sharad Gajbhar	2011
13	Modha Sandip Jayanttilal	2012
14	Padiya Trupti Jayanttilal	2012
15	Tanvina Bhupendrabhai Patel	2012
16	Shikkenawis Gitam Chandrahas	2012
17	Patel Hardik Nayankumar	2012
18	Kapadiya Mayankkumar Chunilal	2012
19	Krishna Gopal	2012
20	Nupur Jain	2012
21	Shah Hiravkumar Jagatbhai	2012
22	Pande Sneha Pramod	2012
23	Vandana Ravindran	2012
24	Dixita Hasmukhbhai Limbachiya	2012
25	Jadeja Mahipal Prithvisinh	2012



26	Nileshkumar	2012
27	Sailor Hardik Bhupendra	2013
28	Kamal Manharlal Captain	2013
29	Vineet Kumar Dad	2013
30	Parth Mehta	2013
31	Shah Monika Gunvantbhai	2013
32	Thakkar Harsh V Rajesh	2013
33	Shah Nirmesh Jayeshkumar	2013
34	Koringa Ppurvi A.	2013
35	Archana Nigam	2014
36	Sumukh Bansal	2014
37	Patel Nikitaben Ratilal	2014
38	Hardik Gajera	2014
39	Desai Nidhi Nitinbhai	2014
40	Rahul Vashisth	2014
41	Chaudhary Pankaj Prabhubhai	2015
42	Rahul Mane	2015
43	Sujata	2015
44	Sankhavara Jainisha	2015
45	Kamble Madhu Rayappa	2015
46	Rishikant Rajdeepak	2015
47	Madhulika Agrawal	2015
48	Patel Purviben Jayprakash	2015
49	Shaikh Mohammedsayeemuddin Kalimuddin	2015

Research Associates

A total of 53 research associates served as project staff on sponsored projects.

Year	Project Staff
2012	31
2013	13
2014	04
2015	05
Total	53



Detailed list of Doctoral Students:

Sr.No.	Name of Employees	Year of registration
1	Janki Akhani	2012
2	Vibha Prajapati	2012
3	Maulik Madhavi	2012
4	Dr. Indrani Chaudhury Singh	2012
5	Tanvina Patel	2012
6	Nitin Ramrakhiyani	2012
7	Parth Mehta	2012
8	Nilesh Vaishnav	2012
9	Hardik Sailor	2012
10	Nirmesh Shah	2012
11	Swati Talesara	2012
12	Kewal Malde	2012
13	Bhavik Vachhani	2012
14	Vaibhav Joshi	2012
15	Parth Gupta	2012
16	Vishal Dave	2012
17	Pankaj Dhalvaniya	2012
18	Rahul Bhaduriya	2012
19	Madhuresh Mishra	2012
20	Asmita Dholariya	2012
21	Rinni Pandya	2012
22	Dhwani Dalal	2012
23	Krupa Barot	2012
24	Jaydeep G Pandya	2012
25	Aarsee Aeron	2012
26	Harsh Trivedi	2012
27	Aanal Patel	2012
28	Hemil Shah	2012
29	Aditi Shah	2012
30	Miten Shah	2012
31	Mital Mistry	2012
32	Ashish Phophalia	2013
33	Rohan Nagrani	2013
34	Roma J Zala	2013
35	Ankur Undhad	2013
36	Shubham Sharma	2013
37	Laksmipriya V K	2013
38	Anusha Pathak	2013
39	S. Nivedita	2013



40	Purvi Koringa	2013
41	Bhaveshriba Chauhan	2013
42	Gayatri Prajapati	2013
43	Maulik Patel	2013
44	Bhumika Chauhan	2013
45	Mohammadi Zaki	2014
46	Avni Rajpal	2014
47	Pramod Bachhav	2014
48	Ankit Nagpal	2014
49	Pankaj P Chaudhary	2015
50	MaulikRathod	2015
51	TusharKokane	2015
52	Hiral Parikh	2015
53	Jainisha Shankhavara	2015

40. Number of post graduate students getting financial assistance from the university.

TA	Batch	Number
M. Tech	2 nd year	45
M. Tech	1 st year	60
Ph D		24
	Total	129

41. Was any need assessment exercise undertaken before the development of new programme(s)? If so, highlight the methodology.

Development of any new programme at DAIICT is a multi-stage rigorous process with the following stages:

1. Feasibility Study

This stage includes academic viability, market demand, and faculty capability analysis. In addition Institute also performs physical and academic infrastructure requirement analysis and financial analysis.

2. Requirement and Benchmark Study

In this stage, peer programmes across the globe are studied for establishing benchmarks for program input, objectives, and outcomes.



3. Model Curricula Design

A detailed analysis of peer curricula is done along with study of reference curricula if any. Institute's vision and requirement study results are used as guides for preparing a detailed curriculum for the programme.

4. Internal and External Feedback Study

Representatives of all the stake holders, including students, faculty, industry, experts from peer academic institutes, and alumni are engaged in brain storming the curriculum and based on these inputs, curriculum proposal is prepared for the presentation to the faculty and later to the academic council

5. Formal Evaluation and Ratification by the Academic Council

Academic council gives the final approval. The approved curriculum is used as a frame work for devising operational elements such as specific course content creation, lab design etc.

In 2013, DAIICT introduced a new program of B.Tech (ICT) Hons. with a minor in Computational Science. In 2015, two new specializations are added in the M.Tech(ICT) program. These specializations are i) Algorithmics and ii) Signal Processing. It is to be noted that DAIICT already offers M.Tech (ICT) programme with specializations in VLSI and Embedded Systems, Communications Systems, Computer Networks and Machine Intelligence.

42. Does the department obtain feedback from

- a) **faculty on curriculum as well as teaching-learning-evaluation? If yes, how does the department utilize the feedback?**
- b) **students on staff, curriculum and teaching-learning-evaluation and how does the department utilize the feedback?**
- c) **alumni and employers on the programmes offered and how does the department utilize the feedback?**

Yes, the institute has a well-defined mechanism in place to obtain feedback from students, faculty, alumni and employers. A feedback form is circulated to each of



the student for all the courses he/she has registered for the semester before the semester ends. The duration of course evaluation by student is also included in the Academic calendar. The exit feedback is collected from all students when they complete their academic requirements for the degree. The campus placement process collects feedback of employers time to time. All feedback forms are accumulated and compiled by the Dean(AP) office. The compiled feedback result is then communicated to the Director for assessment. The feedback provided by the stakeholders is used for improvement in courses and delivery mechanism, and is also discussed in faculty meetings for improvement in respective programmes.

- a. faculty on curriculum as well as teaching-learning-evaluation? If yes, how does the department utilize the feedback?

The curriculum devise/revise of all our programmes goes through multiple rigorous discussions and brain storming sessions by faculty body. Once faculty body clears the draft curriculum of a programme then it goes to the Academic Council for approval. The entire process of new curriculum formulation or existing curriculum revision is coordinated by Board of Studies.

The teaching-learning and evaluation process is carried out throughout the academic year. All the courses follow choice based credit system. As a result, the teaching practice is structured into **Theory-Tutorial-Practical (L-T-P)**. For example, a course with 3-0-2-4 indicates that the course will have weekly 3 hours of lectures and 2 hours of practical, and the student who has registered for this course will earn 4 credits (note: 2 hours of practical give 1 credit). In the classrooms, the teaching aids involve computer, white board, overhead projector, document camera, and audio system. Course instructor use lecture notes, slides presentations and discussion with students through assignments and projects.

Continuous assessment of each course is done throughout the semester. The instructor announces the assessment mechanism and grading policy in the first week of the semester. Typically, in-semester exams, end semester exam, quizzes, assignments, in-class participation are some of the measures that



help in assessing students' performance. The entire course registration, course grading and semester-wise transcript is managed online by the institute through its E-campus system. Both students and faculty can access the authorized module with their respective credentials.

The Dean office of Academic Programs in consultations with UG committee and PG committee discuss matters related to academic improvements, collect feedback from all stakeholders and address all concerns which help in improving the academic process of the institute.

- b. students on staff, curriculum and teaching-learning-evaluation and how does the department utilize the feedback?

The institute has a proper mechanism through which course feedback of students is collected for all courses in every semester. The course feedback form is circulated to each of the student for all the courses he/she has registered for before the semester ends. The course evaluation period is also announced in the Academic calendar. All feedback forms are accumulated and compiled by the Dean(AP) office. The compiled feedback result is then submitted to the Director for review. The course instructors also receive the summarized feedback for each of their courses which help them to incorporate deficiencies in his/her course, if any. In addition, the programme exit survey questionnaire is collected from every student after completion of his/her credits requirements. The feedback provided by the students is used for improvement in courses and delivery mechanism, and is also discussed in faculty meetings for further improvement in the curriculum.








- c. alumni and employers on the programmes offered and how does the department utilize the feedback?

The institute collects feedback from alumni about the programmes, course delivery, and infrastructure. The alumni board meets periodically and opines on various activities such as programmes, curricula, placement, faculty, staff, infrastructure and juniors. The board consists of faculty representatives who brought the feedback of alumni to Dean(AP)'s and Director's notice. The



institute also collects feedback from employers through its placement process. Employers suggest the need of curriculum updation and introduction of new courses to keep pace of industry requirement. Employers feedback is then communicated to Director through the faculty convenor of Placement office. The institute utilizes all the feedback of alumni and employers, which are being discussed with faculty and with respective concerned people to improve the academic activities.



43. List the distinguished alumni of the department (maximum 10)

	Name :Mr. Jyotiraditya IRS (C & CE) Year of Graduation :B.Tech-ICT, 2007 Current Position : Asst. Commissioner, Revenue Department Govt. of India
	Name :Mr. Vivek Dhoot Year of Graduation : M.Tech-2010 Current Position : Sr. Software Engineer, Electromagnetics Tools Mercedes Benz, Germany
	Name :Mr. Sumit Dagar Year of Graduation :B.Tech-ICT 2008 Current Position : Founder-Kriyate, Social/Design Entrepreneur, Delhi
	Name :Mr. Deepak Jagdish Year of Graduation : B.Tech-ICT 2007 Current Position : Research Affiliate MIT Media Lab, USA
	Name :Mr. Vikas Bagri Year of Graduation : B.Tech-ICT 2006 Current Position : Research Affiliate MIT Media Lab, New Delhi
	Name : Mr. Bhavesh Mangalani Year of Graduation : B.Tech-ICT 2006 Current Position : Co-founder, Delhivery Gurgaon,
	Name : Mr. Swapnil Khandelwal Year of Graduation : B.Tech-ICT 2010 Current Position : Co-founder and CEO, Almaconnect, New Delhi



	<p>Name : Ms. Anupama Panchal Year of Graduation : B.Tech-ICT 2013 Current Position : Co-founder and CTO, Griddle.io Ahmedabad</p>
	<p>Name :Mr. Ravi Pokharna Year of Graduation : B.Tech-ICT 2006 Current Position : Director, Engineering Watch Magazine, New Delhi</p>
	<p>Name : Mr. Pavitar Singh Year of Graduation : B.Tech-ICT 2004 Current Position : Vice President, Product Development Sprinklr, New Delhi, India</p>
	<p>Name : Mr. Sidharth Kothari Year of Graduation : B.Tech-ICT 2013 Current Position : Founder, Appbase, San Antonio, Texas, USA</p>
	<p>Name : Mr. Amit Agarwal Year of Graduation : B.Tech-ICT 2008 Current Position : Tech Lead, Yahoo, San Francisco, CA, USA</p>
	<p>Name :Mr. Ravi Bhatt Year of Graduation : M.Sc-IT 2004 Current Position : Big Data Architect, Corporate and Commercial Betfair, London, UK</p>
	<p>Name : Dr. Sai Teja Peddinti Year of Graduation : B.Tech-ICT 2009 Current Position : Research Scientist, Google, USA</p>
	<p>Name : Mr. Vivek Pabari Year of Graduation : B.Tech-ICT 2006 Current Position : Vice President, Investment Banking,, (Coverage and Advisory), Deutsche Bank, Mumbai</p>
	<p>Name : Ms. Ami Ahalpara Year of Graduation : M-Des 2007 Current Position : Developer, ACOS AS, Norway</p>



	Name : Mr. Prateek Kabaria Year of Graduation : M.Sc.-ICT-ARD 2009 Current Position : Marketing & Sales Leader-South Asia Region, Du Pont India
	Name : Ms. Pankti Bindal Year of Graduation : M.Sc.-IT 2008 Current Position : Sr. Software Engineer, Birst India Pvt. Ltd. Ahmedabad

44. Give details of student enrichment programmes (special lectures/workshops /seminar) involving external experts.

The institute has been quite active in inviting eminent educationists and researchers as adjunct faculty/visiting faculty. The institute regularly conducts lectures, organizes symposia, seminars, and workshops. To motivate students and faculty, the institute invites experts regularly to deliver lectures in different research areas. This process enables faculty, students and institute as a whole to sharing research experience, building association and reaching out the knowledge society within the country and abroad. A detailed account of student enrichment programmes is as under:

Sr.No	Title	Dates	Sponsors
1	Workshop on ISCT for Development	25 July 2015	Ministry of Earth Sciences
2	DAIICT-TCS Workshop Series II	17 July 2015	DAIICT
3	Workshop on Intellectual Property Rights	11 July 2015	TIFAC
4	Workshop on Bio inspired Computing	22-24 June 2015	ACM
5	BHUVAN – A Geo-spatial Geo-portal Services	8 May 2015	ISRO
6	DAIICT-TCS Workshop Series I	10 April 2015	DAIICT
7	Winter School on Speech and Audio Processing (WISSAP)	4-7 January 2015	ISCA
8	CRC Press Editorial Workshop	21 August 2014	CRC Press
9	National Workshop on Cyber Security	16-17 November 2013	IEEE
10	DAIICT-SAC Brainstorming Workshop	14 May 2013	DAIICT
11	Using Open Access Resources for Professional Development	16 February 2013	ADINET (Ahmedabad Library Network)
12	NPTEL Awareness Workshop	15 January 2013	Classele
13	Workshop on Image Super-Resolution	24-25 August 2012	SAC-ISRO
14	NEI Workshop on Design of CMOS Analog Circuits	11-22 June 2012	DAIICT
15	Basics of Geomatics Using Open Source Software	28 May to 1 June 2012	DAIICT
16	Workshop on Graph and Geometric Algorithms	10-12 March 2012	NBHM

The Institute regularly invites speakers from academic institutions and industry to deliver special lectures on cutting edge areas in engineering and technology. Below is a list of the talks given during the last four years.

Sr.No	Speaker and Title	Dates	Affiliation
1	Prof. Sundararajan Narasimhan	22,23 & 24th June 2015	NTU, Singapore
2	Prof. Vinay Kumar Mittal Nonverbal Speech Sounds: Analysis and Applications	12 May 2015	IIIT Chittoor
3	Prof. V. Ansari Network Enabled Feature Search for High-Speed Face Recognition in Video Sequences	15 May 2015	University of Dayton, USA



Sr.No	Speaker and Title	Dates	Affiliation
4	Prof. S.K. Pal Soft Granular Mining: Concepts, applications and big data issues	17 January 2015	ISI, Kolkata
5	Prof Gaurav Sharma Imaging Arithmetic	8 January 2015	University of Rochester, USA
6	Prof. Srikanth Narayanan Behavioral Signal Processing	3 January 2015	University of Southern California, USA
7	Prof. Sanjeev Khudanpur Automatic Speech Recognition and Keyword Spotting	19 December 2014	Johns Hopkins University, USA
8	Dr. C.P. Ravikumar Challenges and Opportunities in Embedded Systems	24 March 2014	Texas Instruments India
9	Prof. V M Gadre Placeholder representations for functions and why wavelets are so important	5 March 2014	IIT Bombay
10	Prof. Arvind Constructive Computer Architecture: A new approach to R&D of digital systems	17 January 2014	MIT, USA
11	Prof. V.S. Raju Challenges and Opportunities for Indian Engineers	19 November 2013	Former Director, IIT Delhi
12	Prof. V Rajaraman Co-operative Cloud Computing	11 April 2013	IISc Bangalore
13	Prof. Sanjay Bose Routing Strategy for Wireless Networks	29 October 2012	IIT Guwahati
14	Prof. Subhajit Sen The Art and Science of VLSI Chip Design	19 September 2012	DAIICT
15	Dr. Rakesh Kumar Semiconductor Industry Trends	26 July 2012	President of IEEE Solid Circuits Society
16	Prof. S.D. Agashe Derivation of Laplace Transform	28 February 2012	Emeritus Fellow IIT Bombay
17	Mr. Ross Smith Use of games and play in software engineering	10 th January, 2010	Microsoft, USA
18	Prof. Partha Banerjee Metamaterials: from Fantasy to Reality	12 December 2011	University of Dayton, USA
19	Dr. H S Singh Asiatic Lions, Forest Conservation and ICT	12 October 2011	Govt. of Gujarat
20	Prof. Phani Tetali Game Design – A Case Study	9 September 2011	Industrial Design Centre – IIT Bombay



Sr.No	Speaker and Title	Dates	Affiliation
21	Dr. Amit Sengupta ICT and Electronics for Affordable Health Care	27 August 2011	Tata Memorial Hospital
22	Dr. A.S. Kiran Kumar Chandrayan Mission	8 August 2011	SAC-ISRO

45. List the teaching methods adopted by the faculty for different programmes.

The institute has adopted the following universal mechanisms to make the teaching-learning process student-centric.

- Class room lecturing
- Project based learning
- Assignments and Lab practices
- Peer discussion
- Participation in students clubs
- Research paper study and student presentation
- Self-study – applicable to PhD and M.Tech students
- Internships
- Extra-curricular activities

Two major challenges in an effective teaching pedagogy are large class size and easy availability of online study material. Large classes are challenging for simultaneously handling the needs of a relatively diverse audience, effective interactivity, and ensuring attentiveness of the students. Availability of online study materials creates a perception that face to face lecture time may not be required at all and has the added benefit of flexible timings.

In large classes that we teach, faculty have found that an ICT tool like “personal response system” (PRS) is very helpful in real-time interactivity and thus the possibility of change in teaching emphasis on the fly. We also find that breaking the



lecture in 3 fifteen minutes capsules with interactive games and quizzes of five minutes each greatly enhances the alertness level and the comprehension of the students.

Some faculty have experimented with “flipped classroom” methodology for teaching advanced technical electives with small enrolment levels. Wikipedia defines flipped classroom as an instructional strategy and a type of blended learning that reverses the traditional educational arrangement by delivering instructional content, often online, outside of the classroom and moves activities, including those that may have traditionally been considered homework, into the classroom” We have found that by providing the study material, including lecture notes, online videos, and research papers for the students to study on their own before they come to class, the lectures can be turned into problem solving, ideation, and brain-storming sessions. This has resulted in an enhanced satisfaction level of the students for the course and a superior achievement of the learning outcomes.

Another interesting mechanism used in some courses is the use of narratives. In some of the humanities and social sciences courses, concepts are taught through anecdotes and real life news rather than only theories. In other words, the pedagogical approach is that of storytelling. All concepts are taught by narrating stories or relating them with everyday incidences. The advantage is that, students remember the concepts for much longer time.

Our faculty is continuously looking for innovation in pedagogical devices that best fit the individual characteristics of the courses and the students.

46. How does the department ensure that programme objectives are constantly met and learning outcomes are monitored?

The institute is committed to deliver outcome based quality education and to produce self-reliant knowledge pool by practicing an effective teaching-learning mechanism and producing novel research out comes through a dedicated faculty body, state-of-the-art infrastructure and support of the academic administration. The institute has recently constituted an Internal Quality Assurance Cell (IQAC), which along with various internal committees, guided by the Board of Studies and the Academic



Council can assure standards and quality in all the matters related to academic processes.

The academic calendar of the institute is prepared by the Registrar in consultation with the Dean(AP) and get it approved by the Academic Council prior to the academic year. The calendar outlines the semester schedule, examinations schedule, and course evaluation schedule. The course allocation to faculty for the academic year is prepared by the Dean (AP), with the input of faculty, UG and PG committees, and informed to students prior to the registration of a semester.

The timetable of Lectures, Labs and Tutorials is made available to students, faculty and staff well in advance before the commencement of a semester. Most of the instructors communicate to students about the lesson plan, topics to be covered including methodology and the evaluation process for the course at the beginning of the semester.

The performance of the students is assessed on a continuous basis by conducting in-semester exams, end-semester exam, assignments, quizzes, student presentations, and projects. A minimum of 80% attendance is required in each course for appearing for the end semester examination. After evaluation, the answer books are shown to the students so that they can see where they have made mistakes and discuss with the instructor accordingly. The semester result is announced within a week after the end semester examination.

Dean(AP), UG committee and PG committee continuously monitor all academic activities. They discuss with faculty and Director for all concerns pertaining to programme objectives and outcomes. The alumni of the institute have already established the brand of DA-IICT in industry, R&D organizations and other institutions in India and abroad about the quality education offered at the institute. With a dedicated faculty team, high-caliber students input, excellent infrastructure, and the enriching curriculum of the programmes, the institute is able to produce graduates with adequate knowledge and hands-on skills in information and communication technology by which they can establish themselves as successful professionals in industry and R&D organisations. As a result, the institute ensures that programme objectives are constantly met and learning outcomes are supervised



throughout the academic calendar.

47. Highlight the participation of students and faculty in extension activities

During the rural internship, students often carry out surveys to help NGOs in setting the baseline conditions. In fact, the NGOs have been helped in developing teaching modules, training workers, writing up small software programs, preparing documentation and many more activities by the students during the rural internship because the students devote one full month to the NGOs to carry out whatever work they have.

In addition, the Institute has helped many voluntary organizations in designing their programs with its expertise so that they could reach the underprivileged more effectively. For example, four students did their BTP project for AkshayPatra, to chart out optimal path for the vans that reach out to almost 1 lakh students in various schools. Similarly, the government's e-gram centers were helped by the students in sprucing up their facilities – first the needs assessment for the rural population was done and later software programs were developed to be included in the e-gram network. Our students have helped deaf and dumb school by gathering videos and educational games for their students and conducting training for the instructors, developed a software for a small bank in Saurashtra for its human resource management because it had no access to such programs, carried out surveys regarding environment effects of Mundra port for Center for Environment Education, and such. The examples are too many to site here because many different courses – technical as well as social science – include such exercises that reach out to the outside community.

One more example was to examine how do we represent health as a problem, defined by a community which allows for community activism and agency and yet can be seen as a policy problem. What one basically learnt from the conversations is that design can mediate between the right to Information and the possibilities of representation. Mere transparency does not guarantee the right to information about health. It needs a competence to play with parameters of health to effect new connections and consequent changes in policy. Design thus becomes a methodological tool not just for policy but peoples' engagement with it. If Design is



connectivity, one had to see all the relations that exist about health - connectivities between health and water, health and land, natural resources, agriculture, food consumption including policies were established in the project that became the key connections through which health of a community were monitored. The project was driven largely with an aim to work on prevention rather than cure, and finally one needed to capture the dynamic conditions between altered conditions. If health was change, one needed to understand the changing relations between agricultural practices and consumption of food and/or water and health. Design also took into account time and the long duree of health. For example one could increase resistance to sickle cell even if one could not eliminate it. The project was conceived as a system that would be monitored by the community themselves.

In addition, the faculty members have worked as advisers in policy formulation, as an extension activity of their own knowledge. Some examples of such activities would be:

- As advisor to the Govt on policy formations, Professor Vishwajit Pandya advices and formulates policies in collaboration with the PVTG tribal groups of Andaman and Nicobar Islands.
- Prof Alka Parikh was a member of Working Group of Planning Commission of India for both 11th and 12th Five Year Plans. She worked on the policies for agriculture in the 11th plan and on disaster management preparation plans for the 12th plan.
- Prof Alka Parikh helps the staff of Utthan (as a member of board of Trustees) and Uplift India Association (as a Joint Director) in formulating their programs for rural development. She also advises the Centre for Social Studies, Surat, as a member of the Governing Board.
- Prof Hiremath is a board member for Jaipur Rug Foundation.
- Prof Aditi Nath Sarkar is the Member of the Governing Council, Satyajit Ray Film & Television Institute (SRFTI) (An Academic Institution of Ministry of I&B Govt.of India)

Youth Run

Youth run was an initiative by a group of students who wanted to promote good health, camaraderie and social awareness. The Youth run vision was to run for a social cause. The run was held every year for three years in the month of February. DA-IICT Students, many from the Gandhinagar community and local school children would participate in a 4km or 8km run in the wee hours of the morning. Around 1000 persons running together in cold during the early morning early made a wonderful scene. The run usually ended with some light snacks and a talk and interaction session with an eminent person on a theme related to social awareness. Profits of Youth run were given to an NGO.

48. Give details of “beyond syllabus scholarly activities” of the department.

The institute constitutes (through a normal election process) a dynamic Students Body Government (SBG) which coordinates many extension activities through various students’ clubs. Each student club is mentored by a faculty in which interested students can actively participate for software development, programming, web development, electronics hobby and so on. Various Technical Clubs at DA-IICT are:

1. Programming Club (Aryans) - a platform to enrich the concepts of computer hardware and software.
2. Electronics Hobby Center (EHC) – hardware explorations.
3. Microsoft Student Technical Club (MSTC).
4. Communication and Networks Club.
5. Web Development Club (WebDev)
6. Google Developers Group (GDG)

Students also actively participate in IEEE student branch and ACM (Association for Computing Machinery) student branch at the institute.



With active participation in national and international forums, our students have received many prestigious awards such as Texas Instruments DSP Design, Microsoft Imagine Cup, Red Hat Challenge, HP Innovate, Google Summer of Code, TCS-100 Best Student, Google India Women and many more.

Students are also encouraged to participate and present technical papers at National/International Conferences organized within and outside India.

49. State whether the programme/ department is accredited/ graded by other agencies? If yes, give details.

No.

50. Briefly highlight the contributions of the department in generating new knowledge, basic or applied.

A sizeable number of books have been published by faculty. These books promote learning and scholarship of a very high order. In the last five years, the faculty authored or co-authored more than 400 articles in books, journals, and conference proceedings. The faculty members have contributed to several national projects funded by DST and DeitY. New courses are regularly introduced in the emerging areas of ICT (e.g., Internet of Things). Workshops are regularly held with a view to sharing the new knowledge generated. A good number of students have turned their technical inventiveness into business ventures.

Amongst the many research projects being carried out by faculty and students, here are examples of two applied projects, which seek to enhance the use of Indian languages in information retrieval. The first project is directed towards the development of a search engine that will allow querying of information from Indian language text documents available on the web. The other project is about developing a search engine for audio databases in Indian languages. Both projects are funded by government of India, and they are being jointly executed with other premier institutions in the country.



51. Detail five major Strengths, Weaknesses, Opportunities and Challenges (SWOC) of the department.

STRENGTHS	WEAKNESS
<ol style="list-style-type: none"> 1. Autonomy in Governance 2. Faculty Profile and Composition. 3. Research Driven Teaching, Learning and Pedagogy. 4. Physical and ICT Infrastructure. 5. High Calibre Student Body and Outstanding Alumni. 	<ol style="list-style-type: none"> 1. Collaborative inter Disciplinary Research. 2. Consultancy, Patents and IPR. 3. International Academic Collaborations. 4. Faculty Residency. 5. Ever-changing Government Policy on Admissions.
OPPORTUNITIES	CHALLENGES
<ol style="list-style-type: none"> 1. Industry institute interface and external linkages. 2. Inter-disciplinary Programmes 3. Creating Centres of Excellence 4. Strengthening Entrepreneurship Initiatives 5. Providing Leadership for the growth of ICT Education 	<ol style="list-style-type: none"> 1. Faculty Retention. 2. Self Reliance in Finances 3. Sustenance as Premier Institute 4. Dearth of Scholarship 5. Foreign Universities in India

52. Future plans of the department.

A detailed strategic plan and SWOC analysis has been prepared. This document highlights the future plans of the Institute for the next five years. Some of the important initiatives to be addressed for the next five years are:

1. Strengthening human resources and academic rigor
2. Enrichment of Research and Development, consultancy, IPR and patents
3. Fortification of industry-Institute connect through national / international collaborations
4. Brand Buildings